

FIG. 1

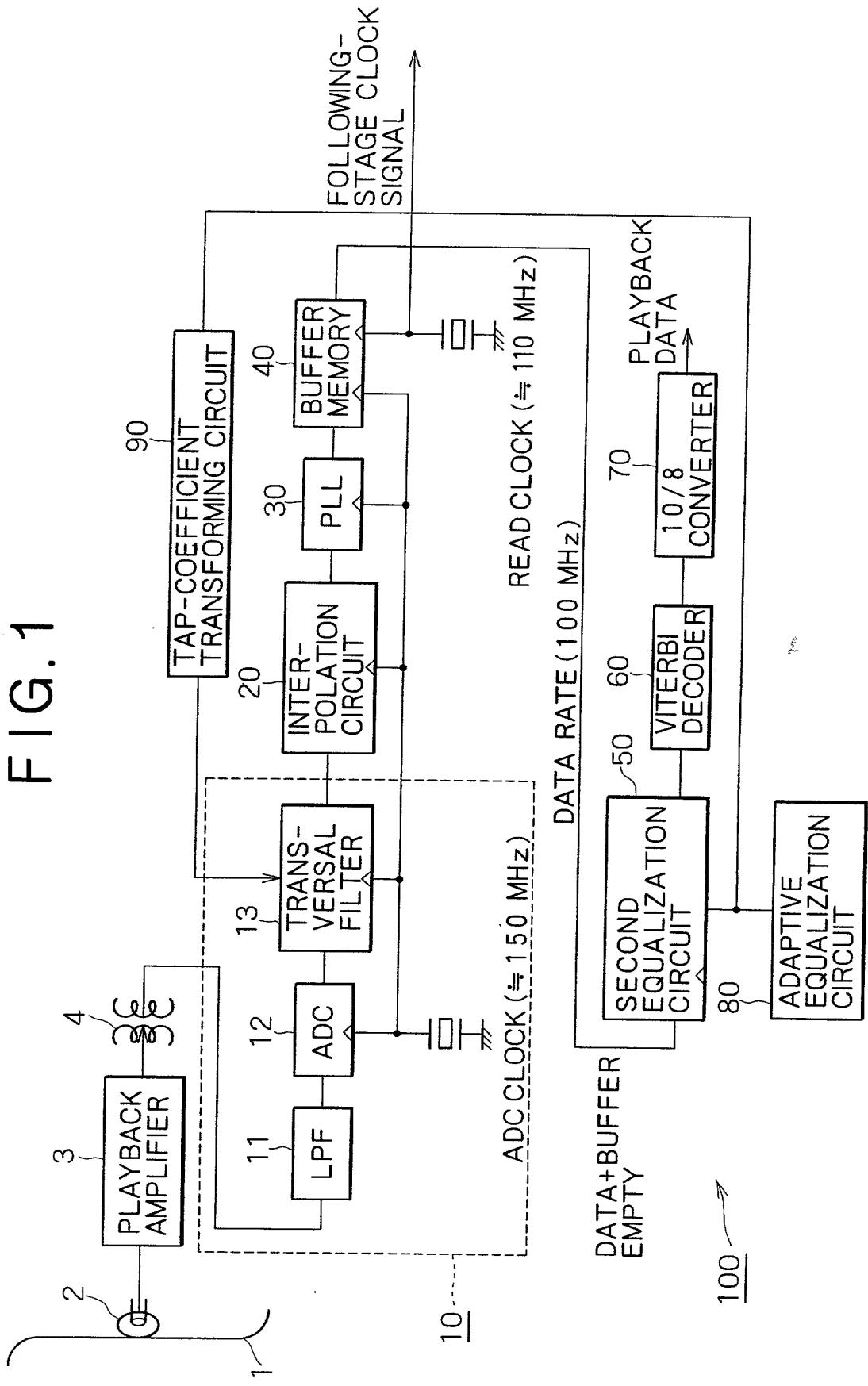


FIG. 2

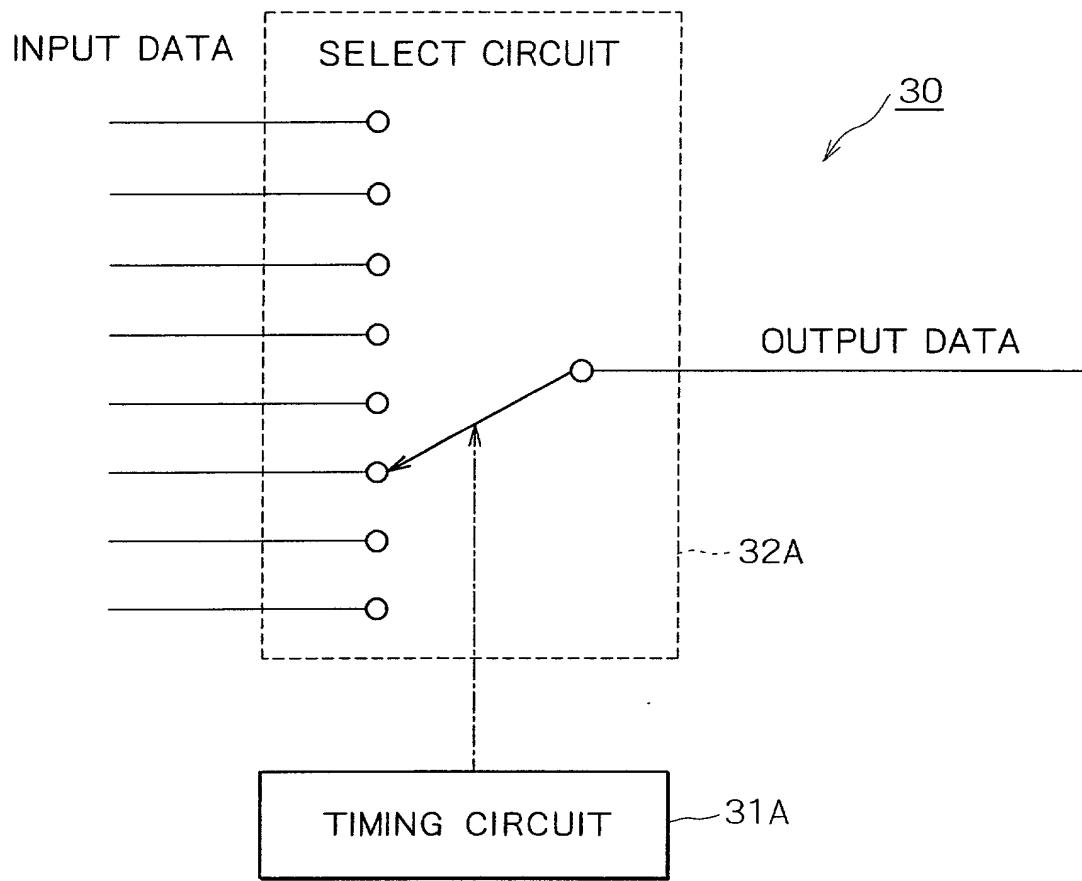


FIG. 3

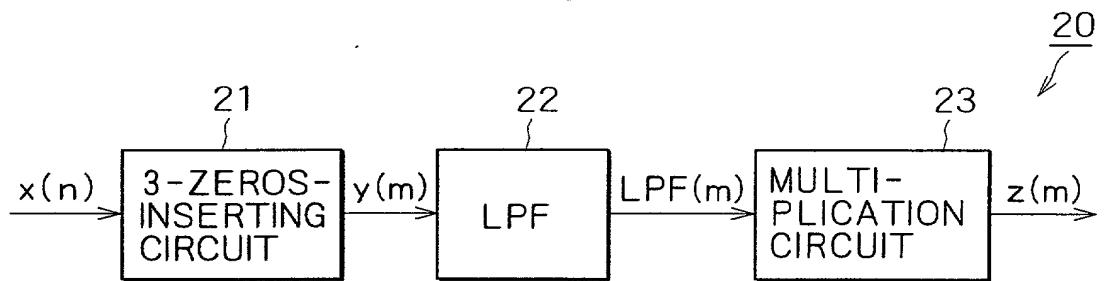


FIG. 4A

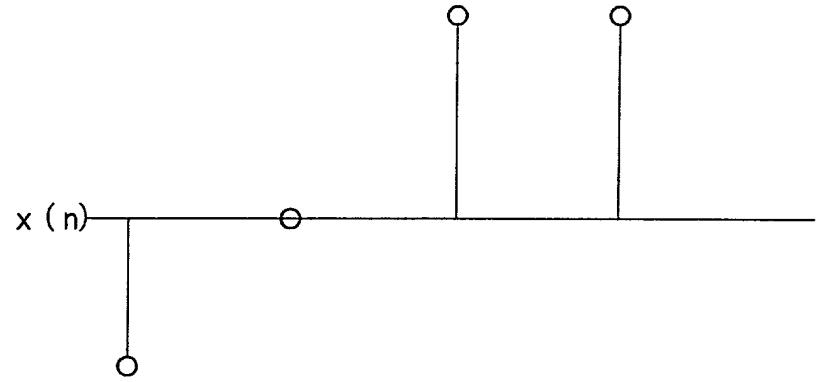


FIG. 4B

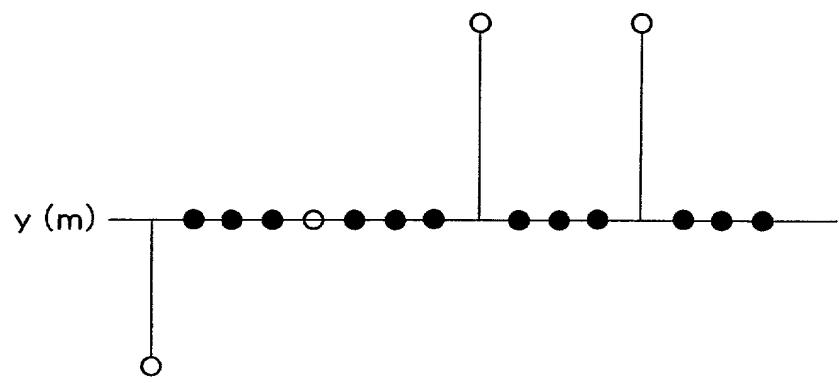


FIG. 4C

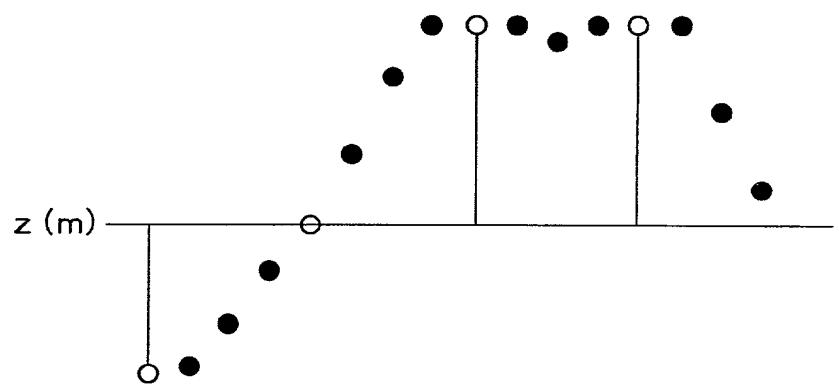
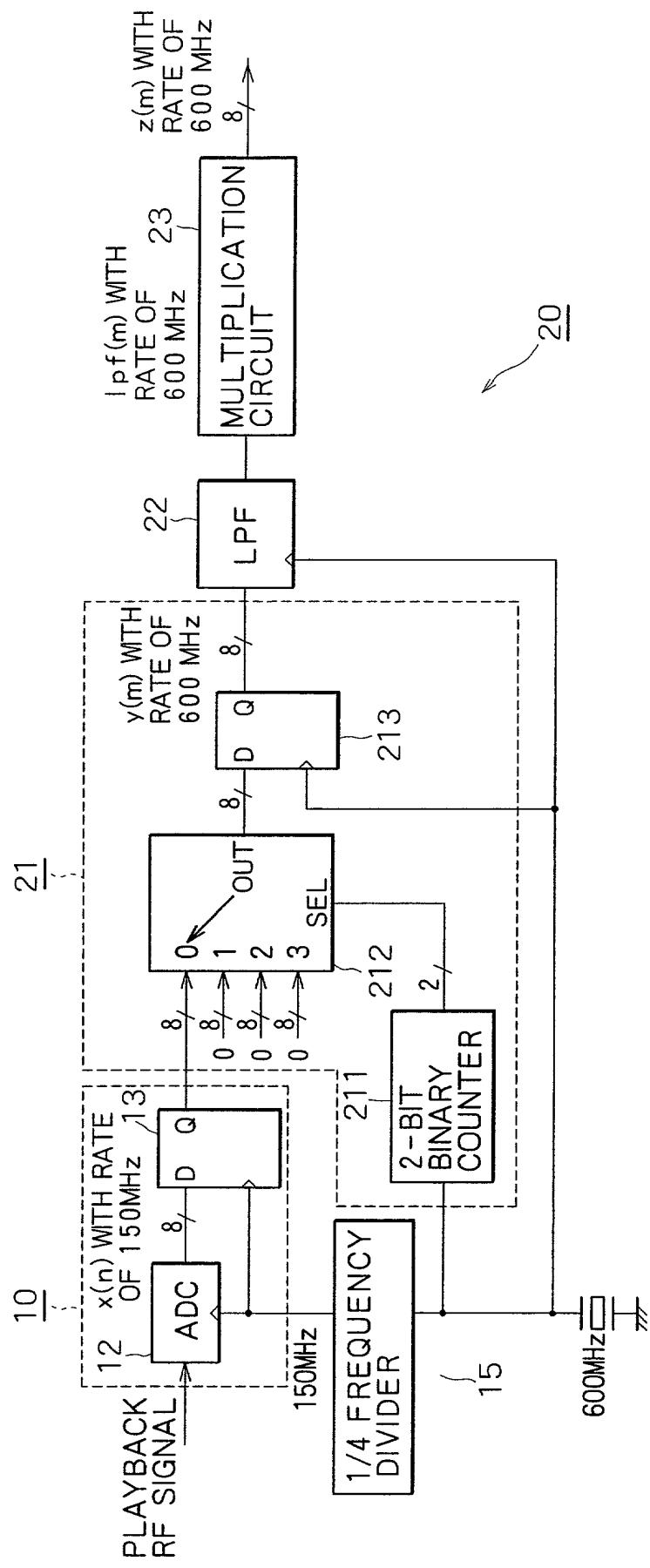


FIG. 5.



65

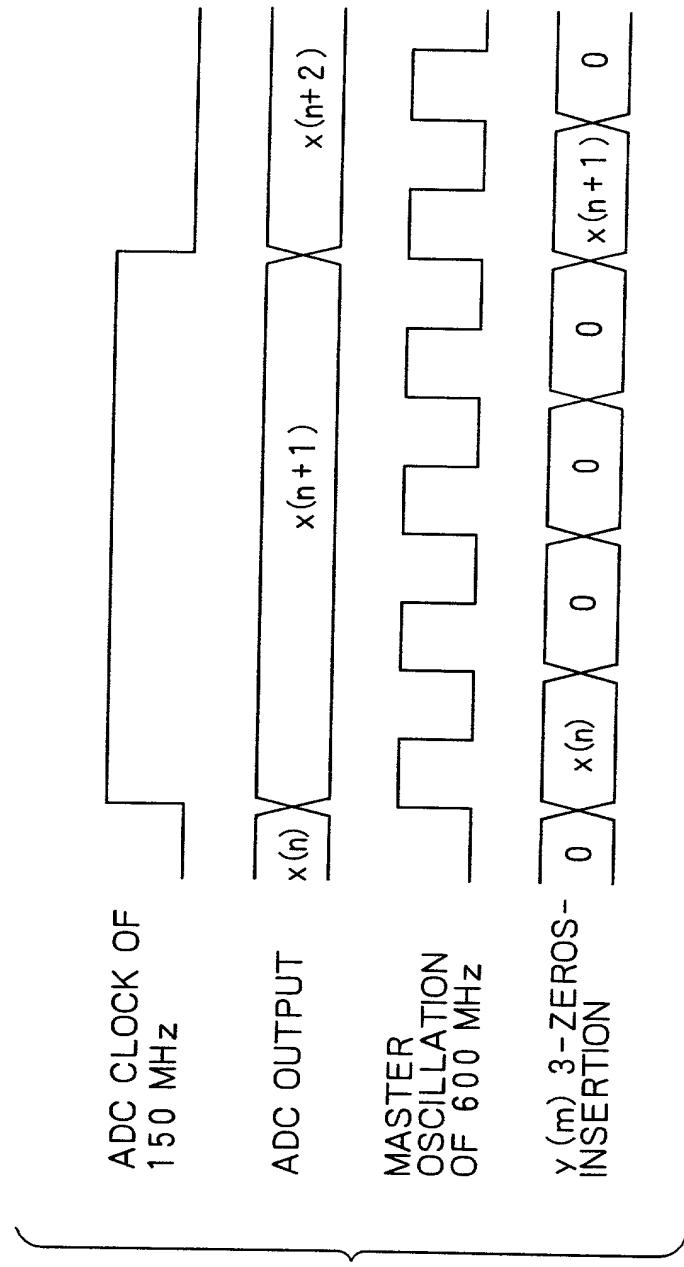


FIG. 7A

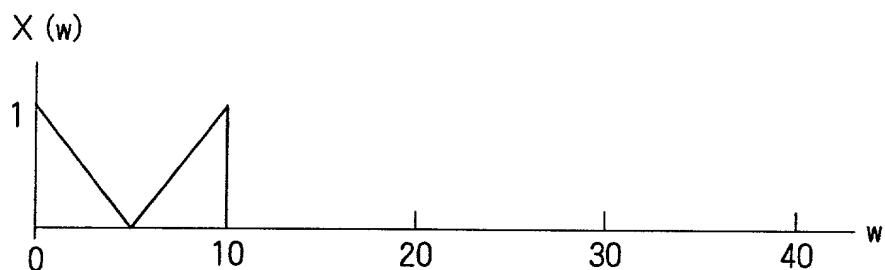


FIG. 7B

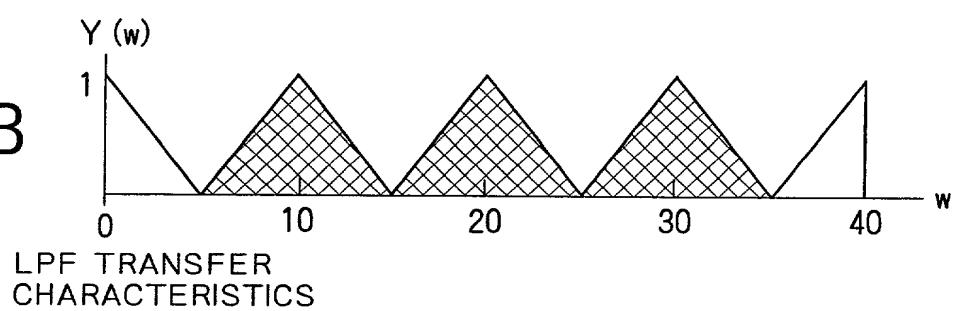


FIG. 7C

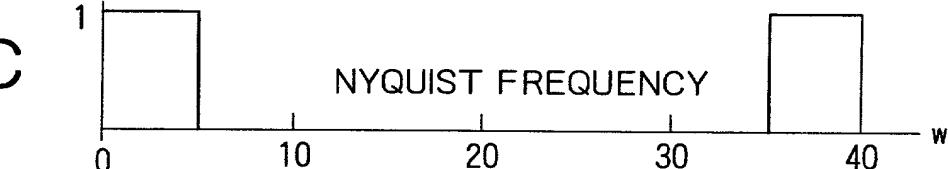


FIG. 7D

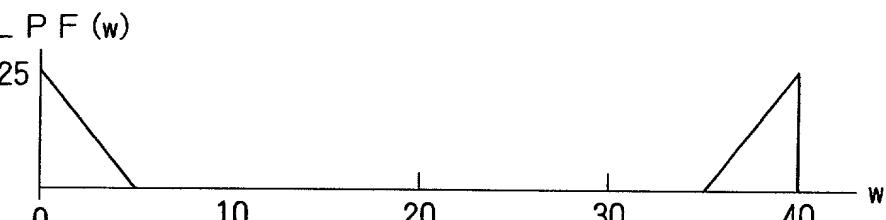


FIG. 7E

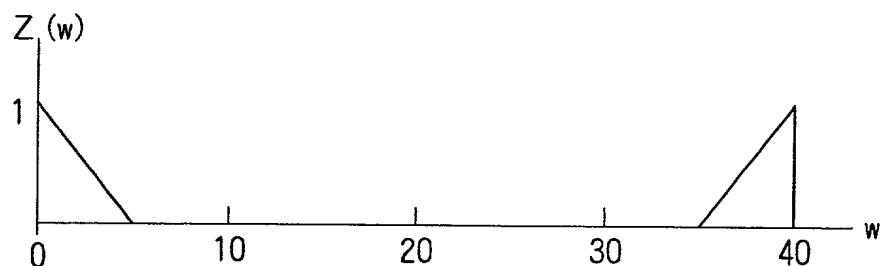


FIG. 8

TRANSFER CHARACTERISTICS OF THE $\times 4$ INTERPOLATION LPF

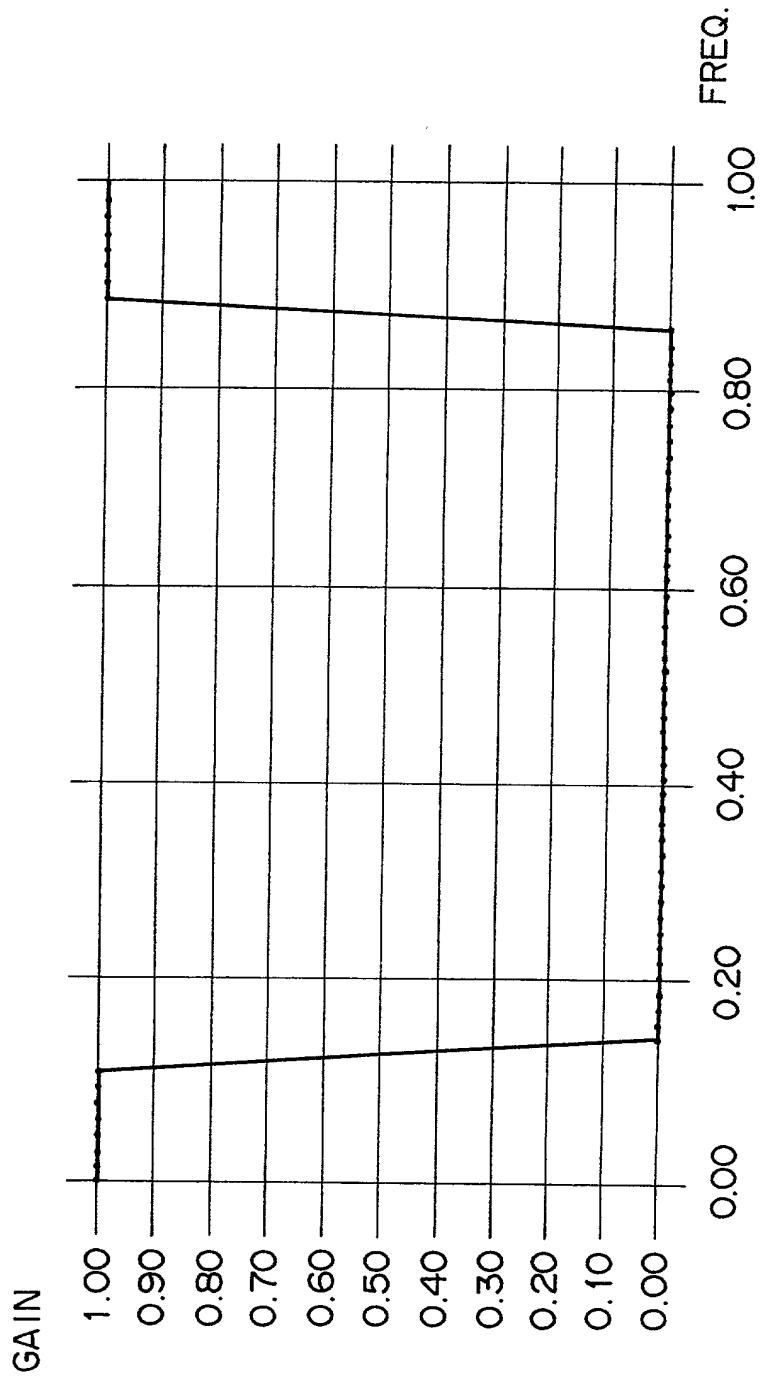


FIG. 9

x4 INTERPOLATION LPF TAP COEFFICIENTS

VALUE

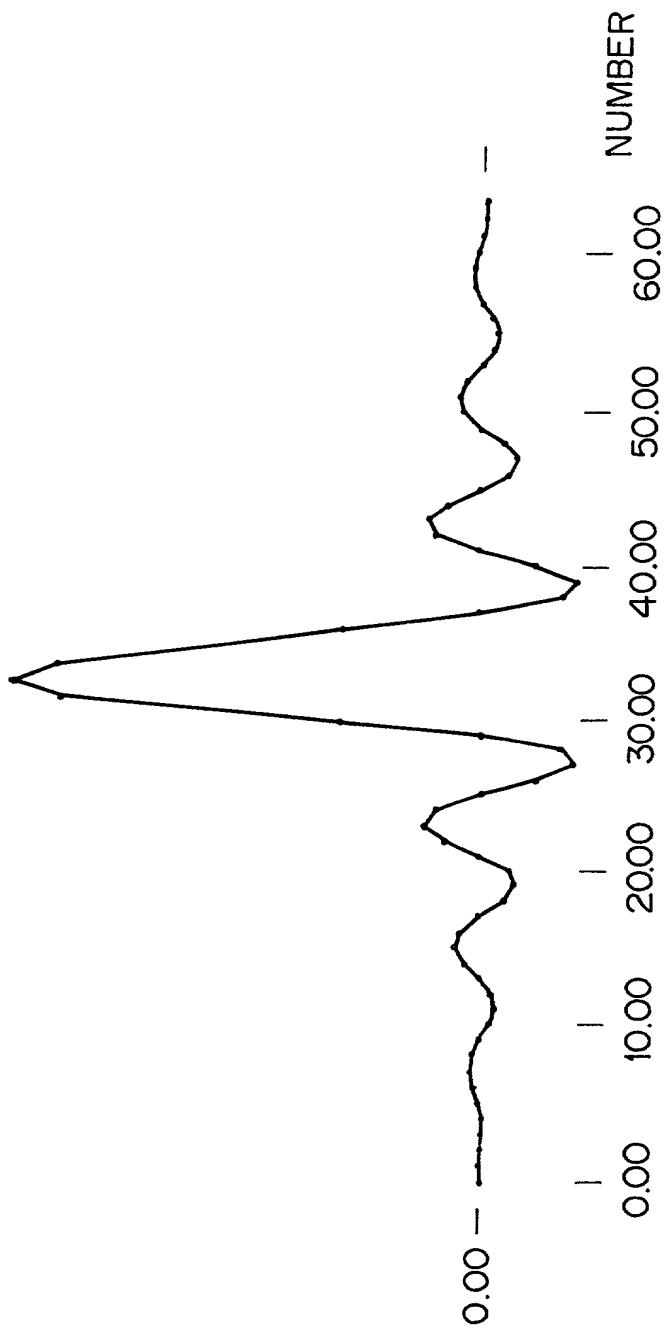


FIG. 10A

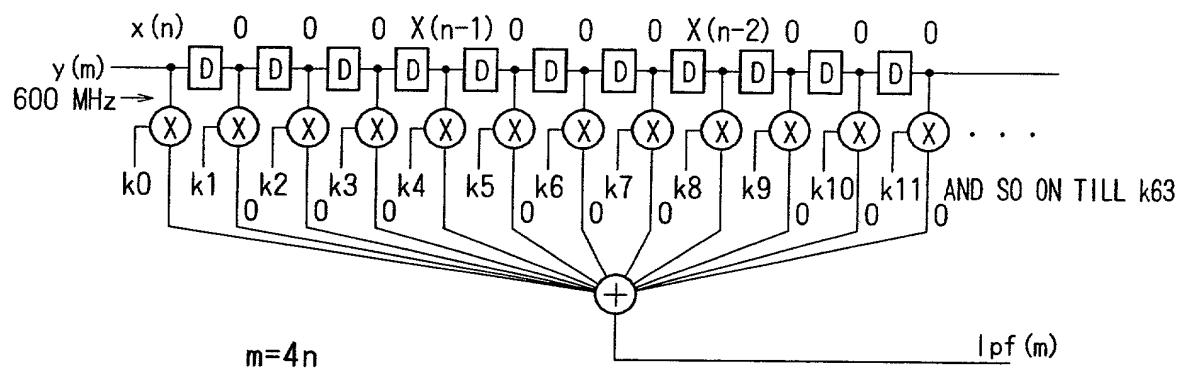


FIG. 10B

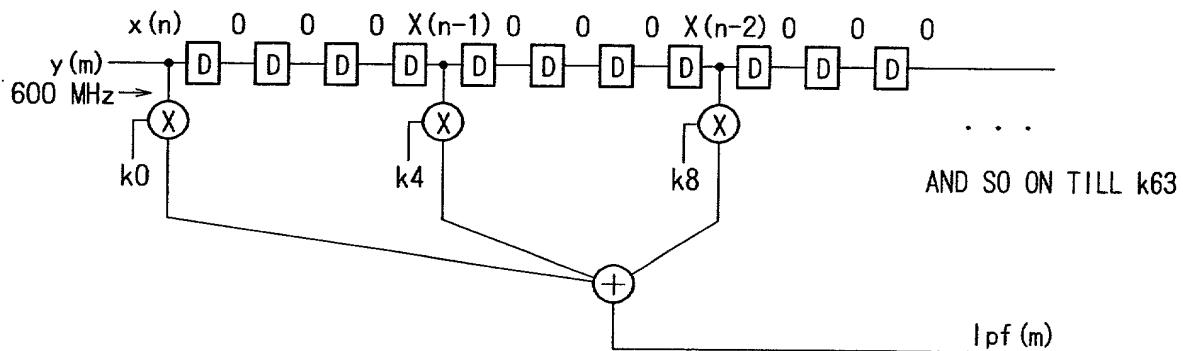


FIG. 10C

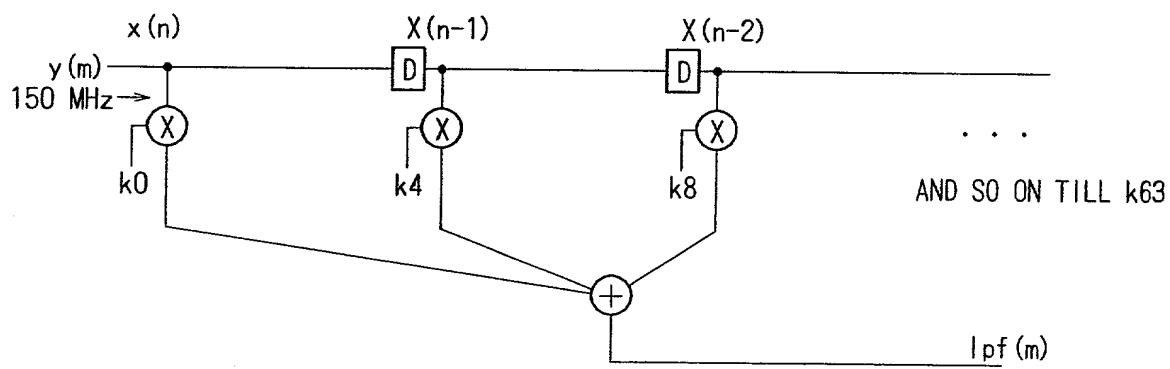


FIG. 11A

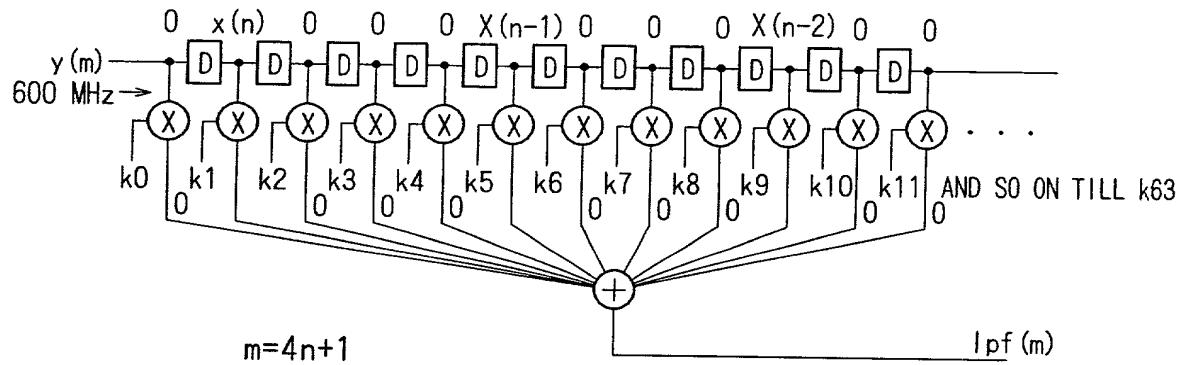


FIG. 11B

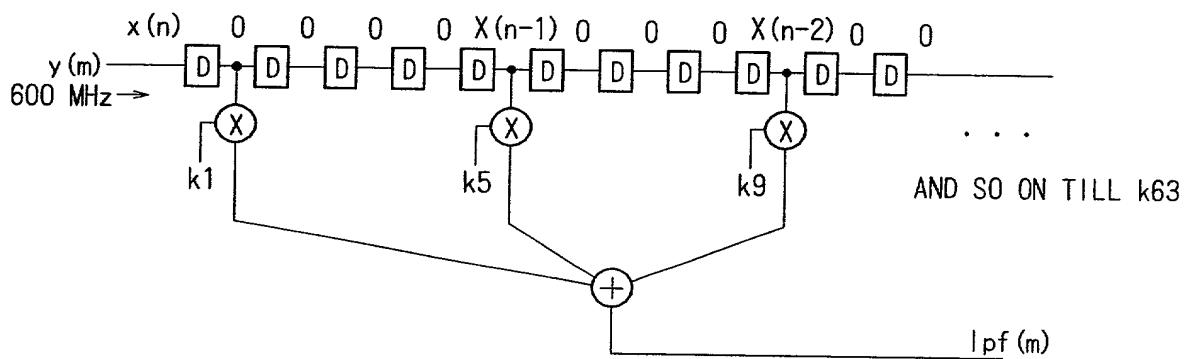


FIG. 11C

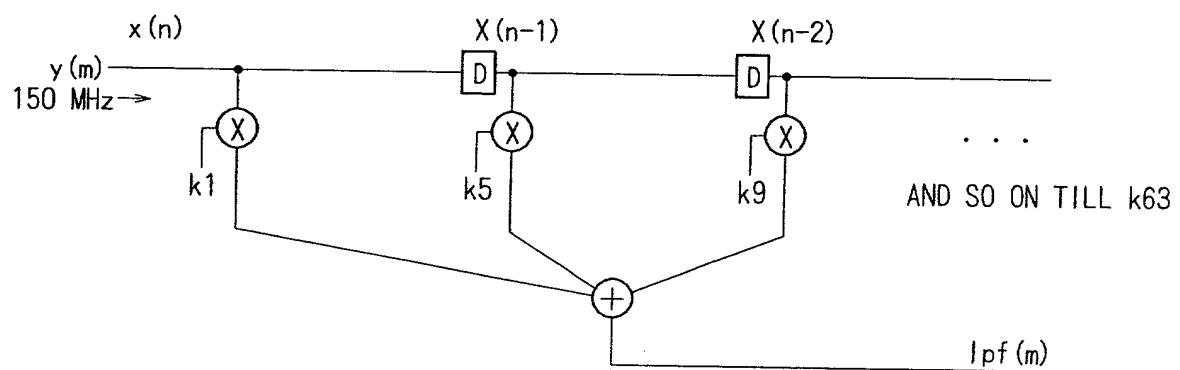


FIG. 12A

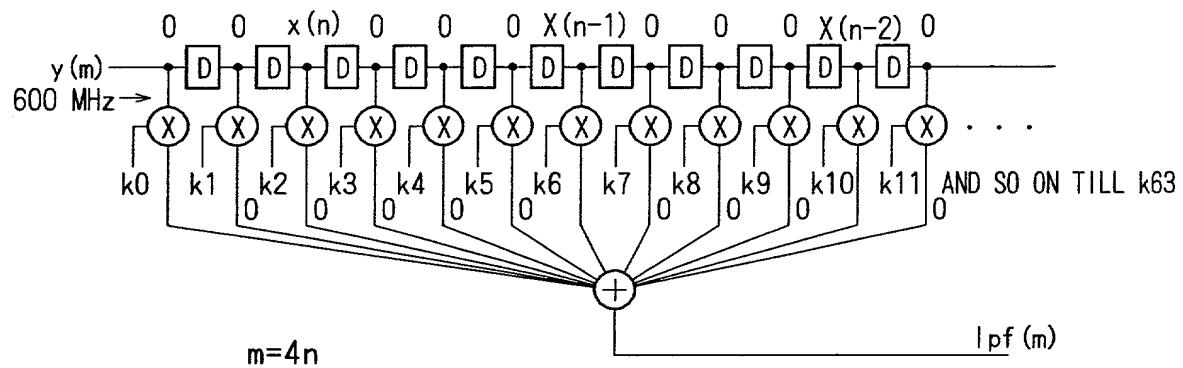


FIG. 12B

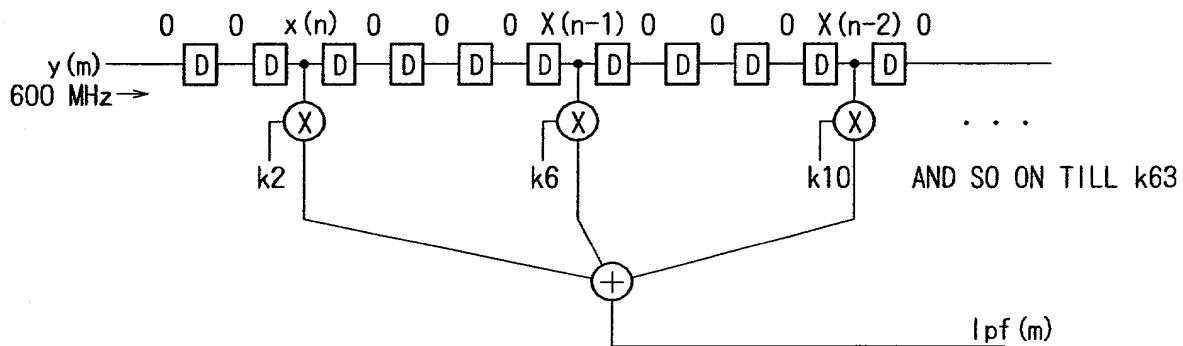


FIG. 12C

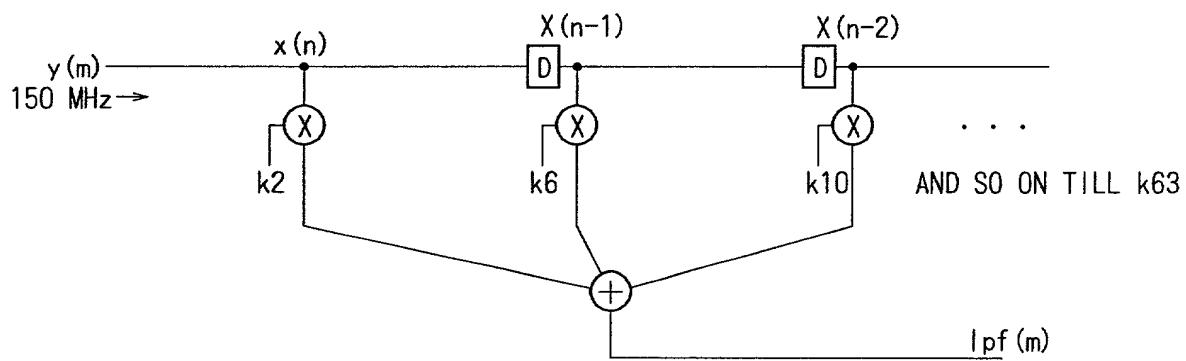


FIG. 13A

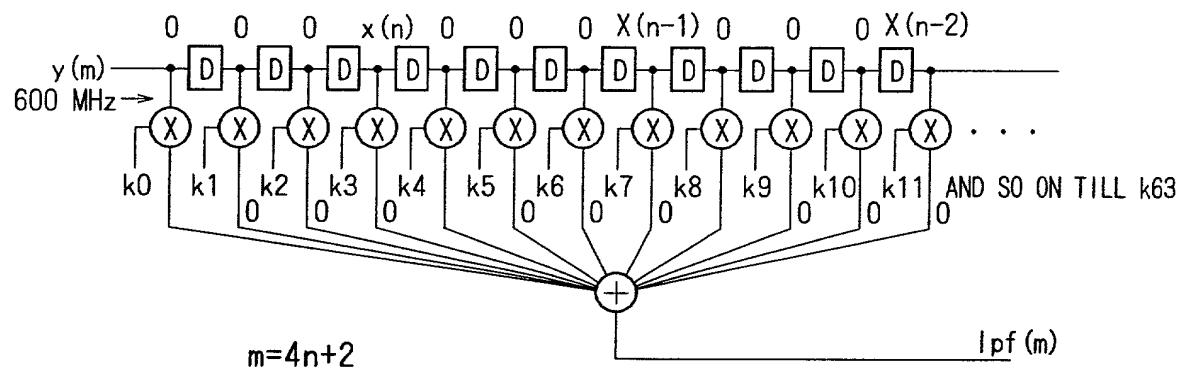


FIG. 13B

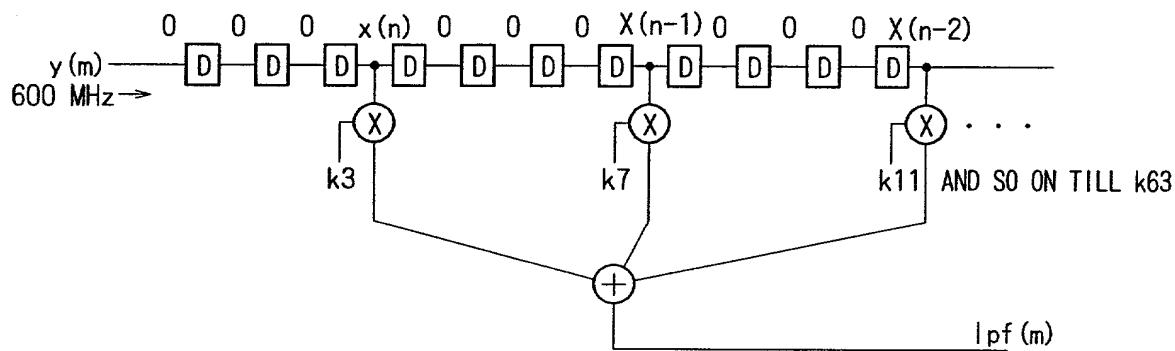


FIG. 13C

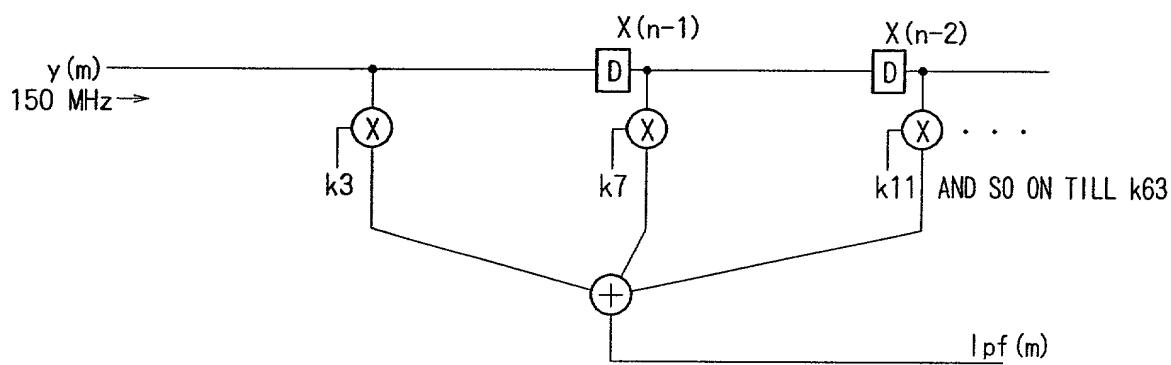


FIG. 14

$X(n)$ WITH
RATE OF
150 MHz

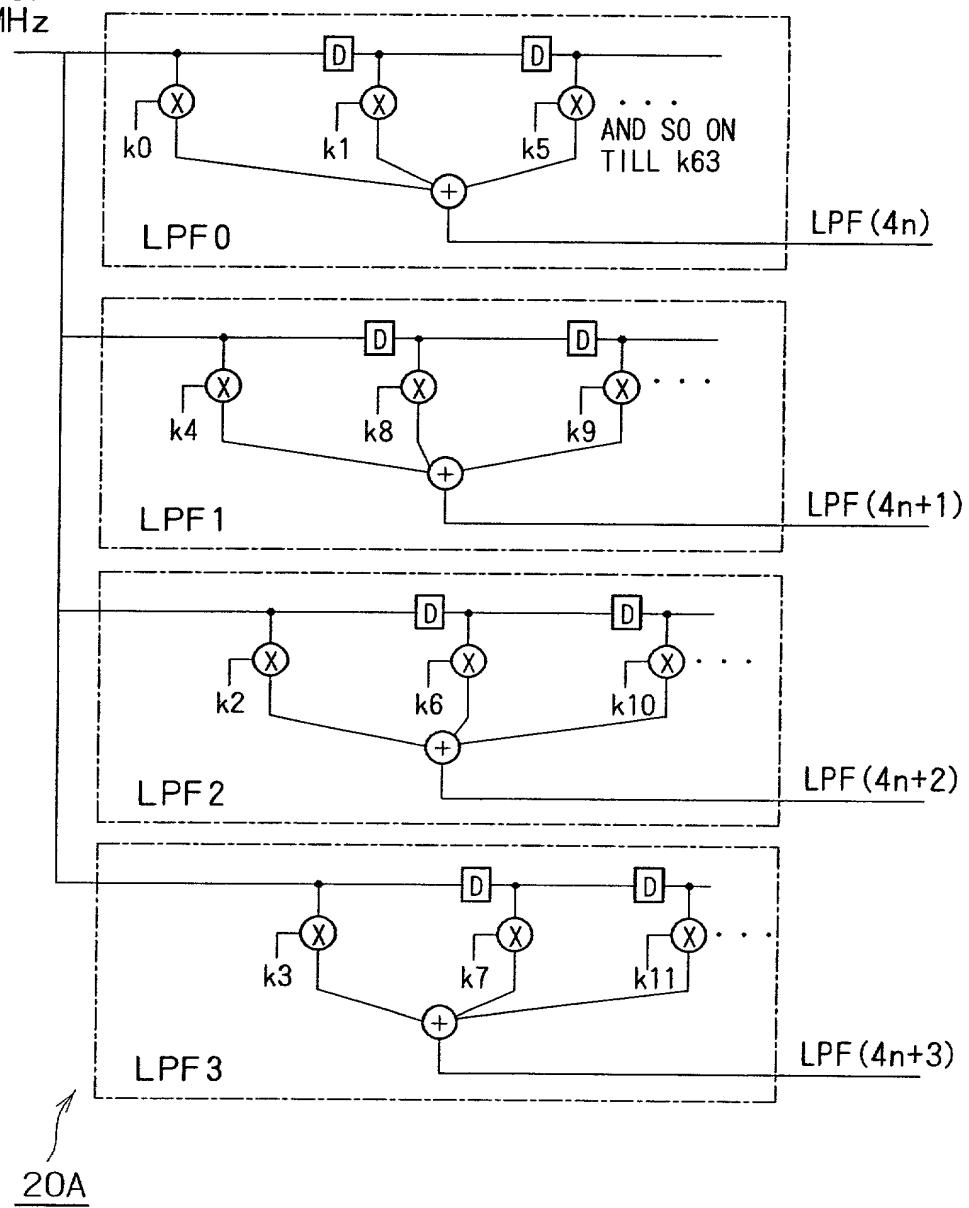


FIG. 15

TAP COEFFICIENT OF THE x4 INTERPOLATION FILTER

TAP VALUE

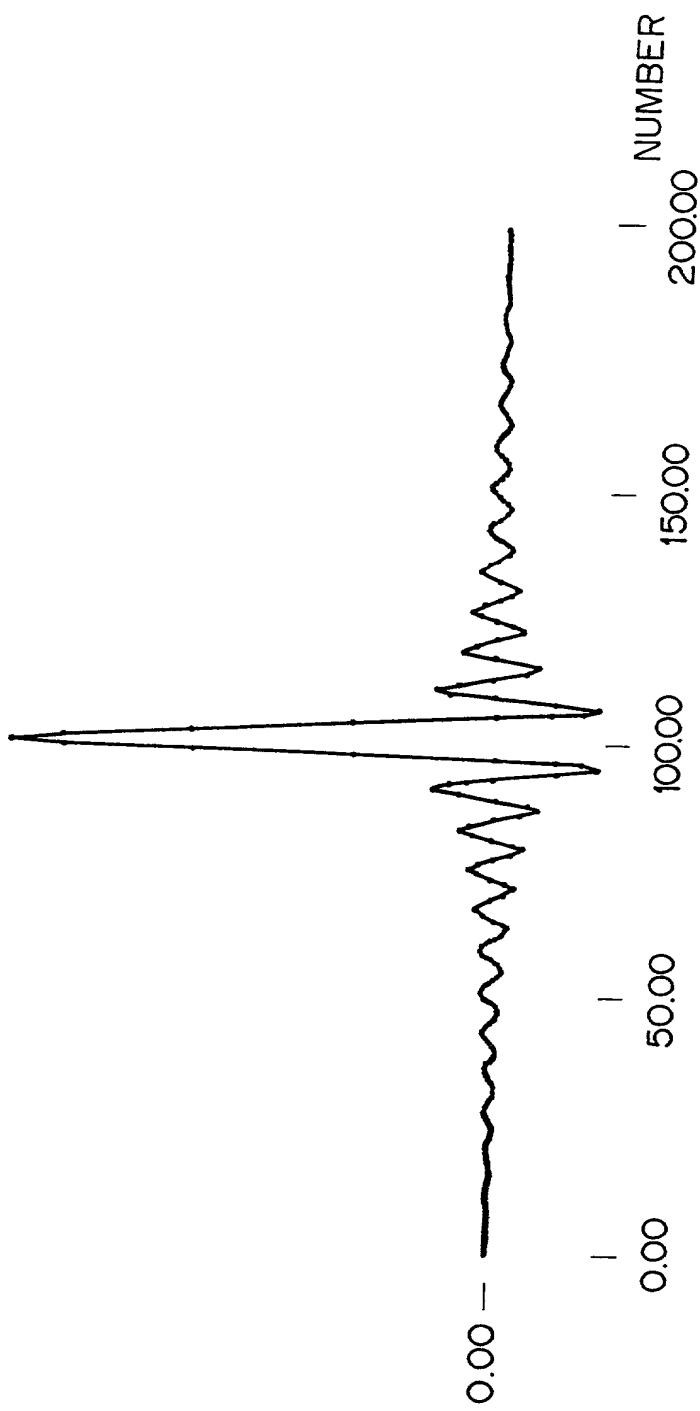


FIG. 16

TAP COEFFICIENT OF THE x16 INTERPOLATION FILTER

TAP VALUE

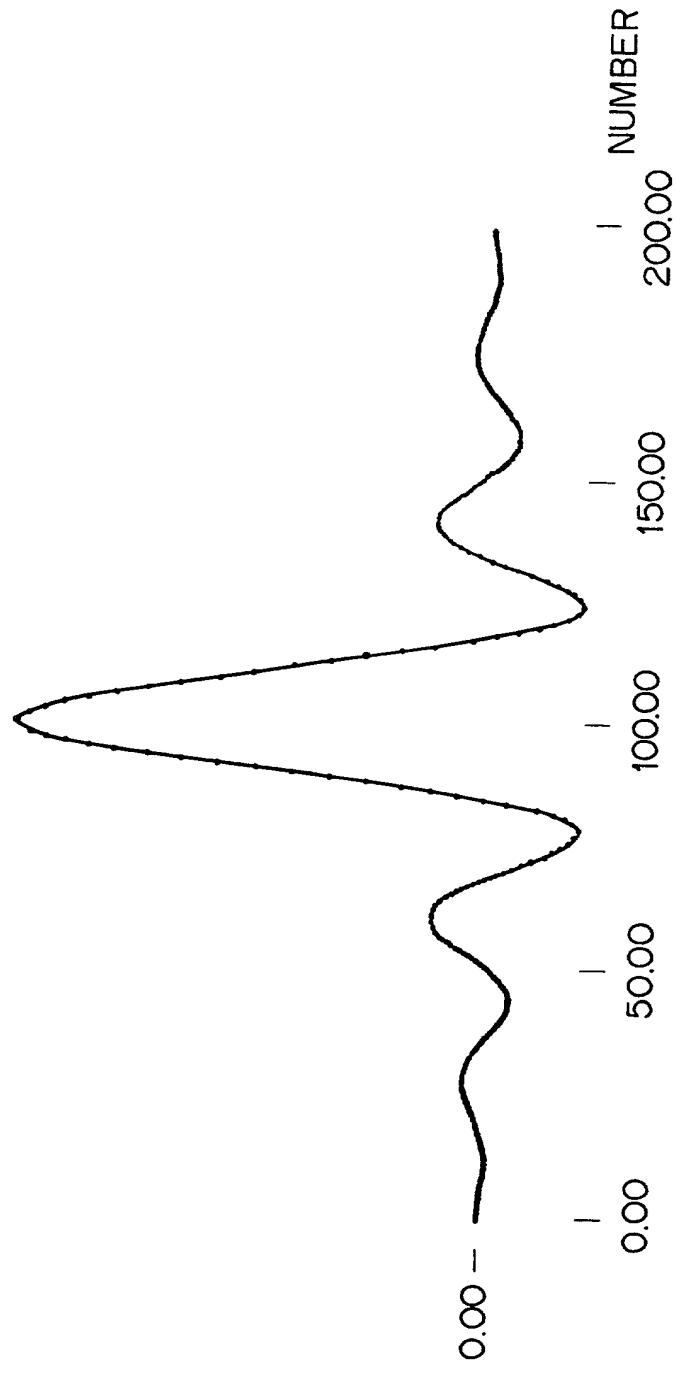


FIG. 17

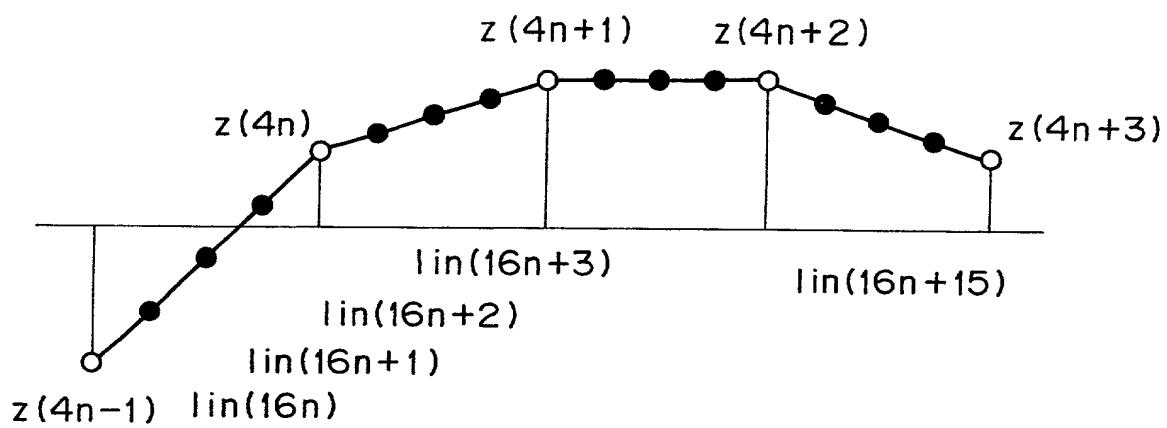


FIG. 18

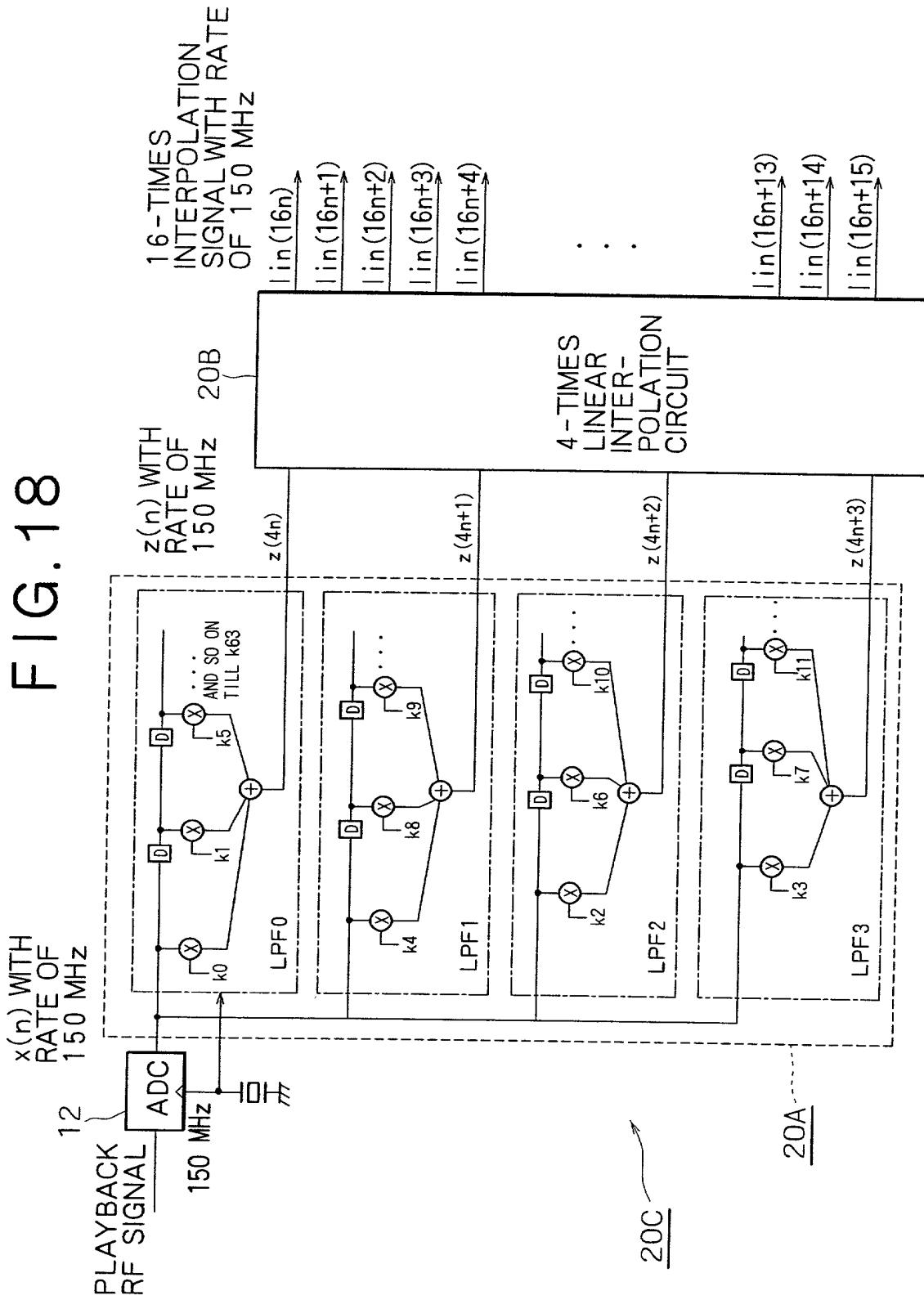
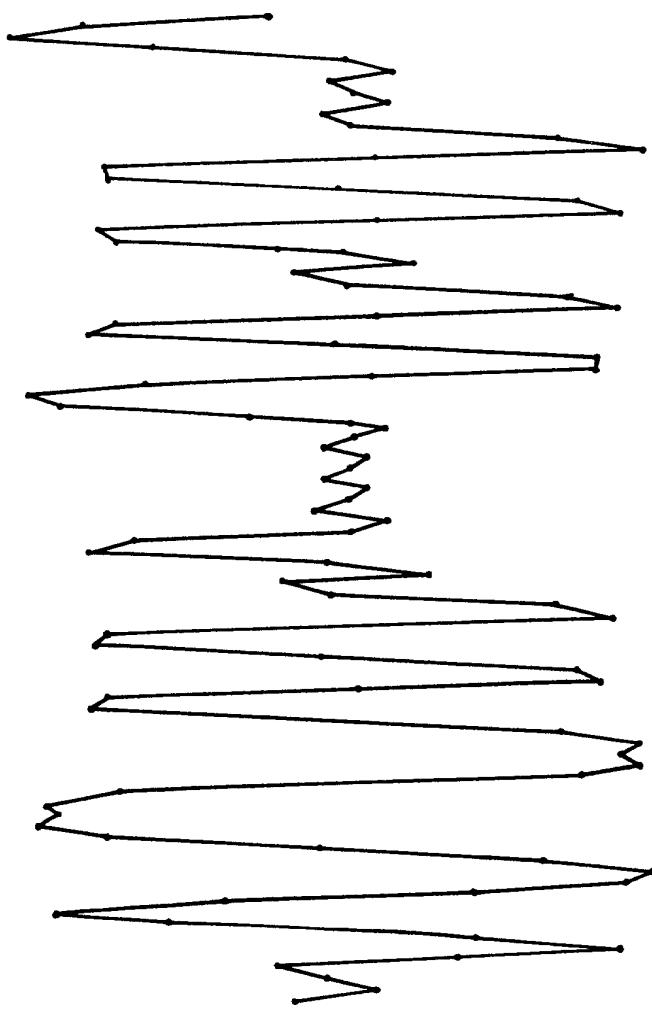


FIG. 19

x1.5 OVER SAMPLED DATA

VOLTAGE

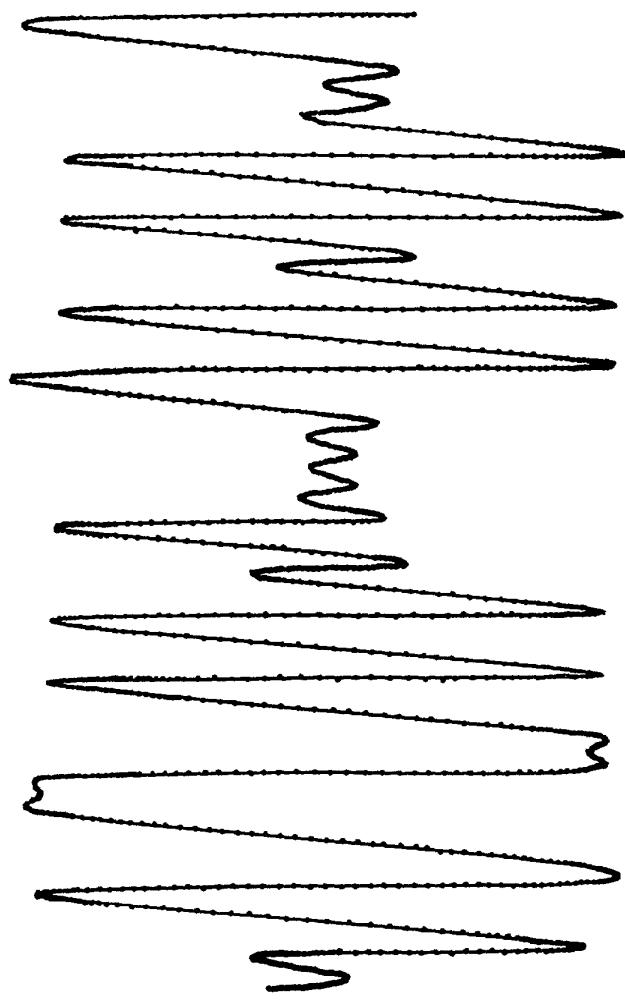


SAMPLING NUMBER

FIG. 20

x16 INTERPOLATED DATA

VOLTAGE



SAMPLING NUMBER

FIG. 21

EYE PATTERN OF THE $\times 16$ INTERPOLATED DATA

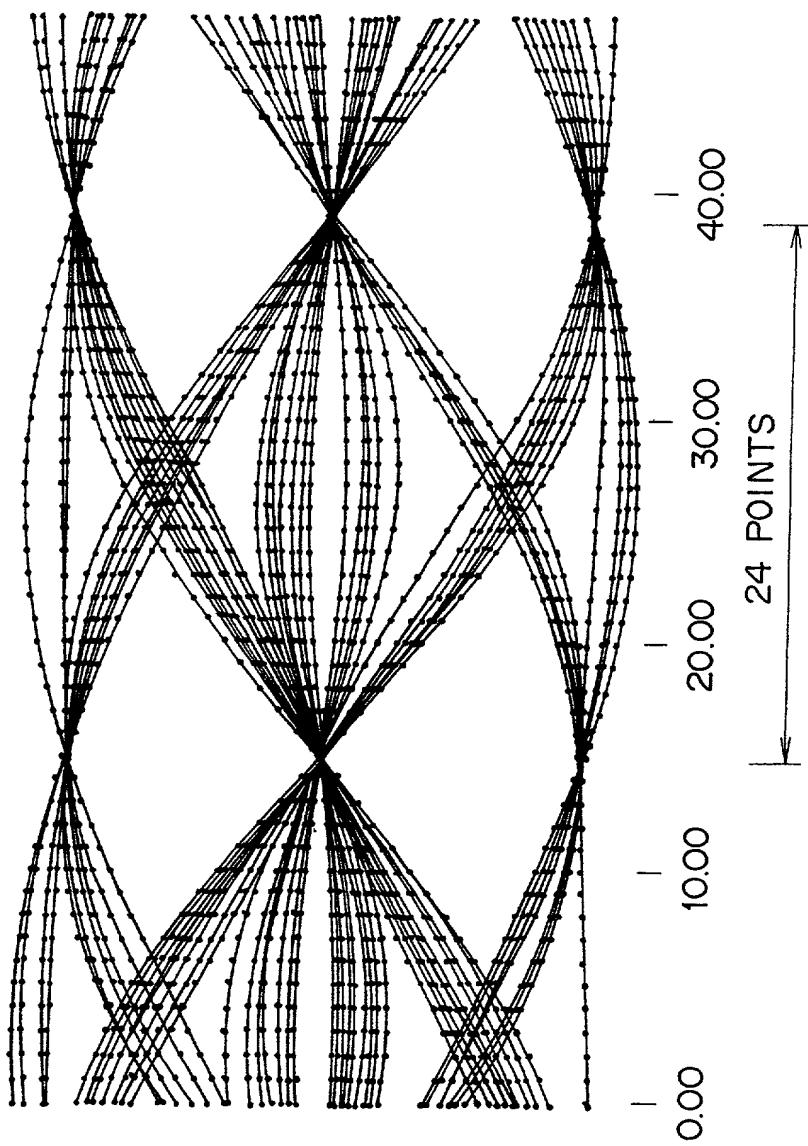


FIG. 22

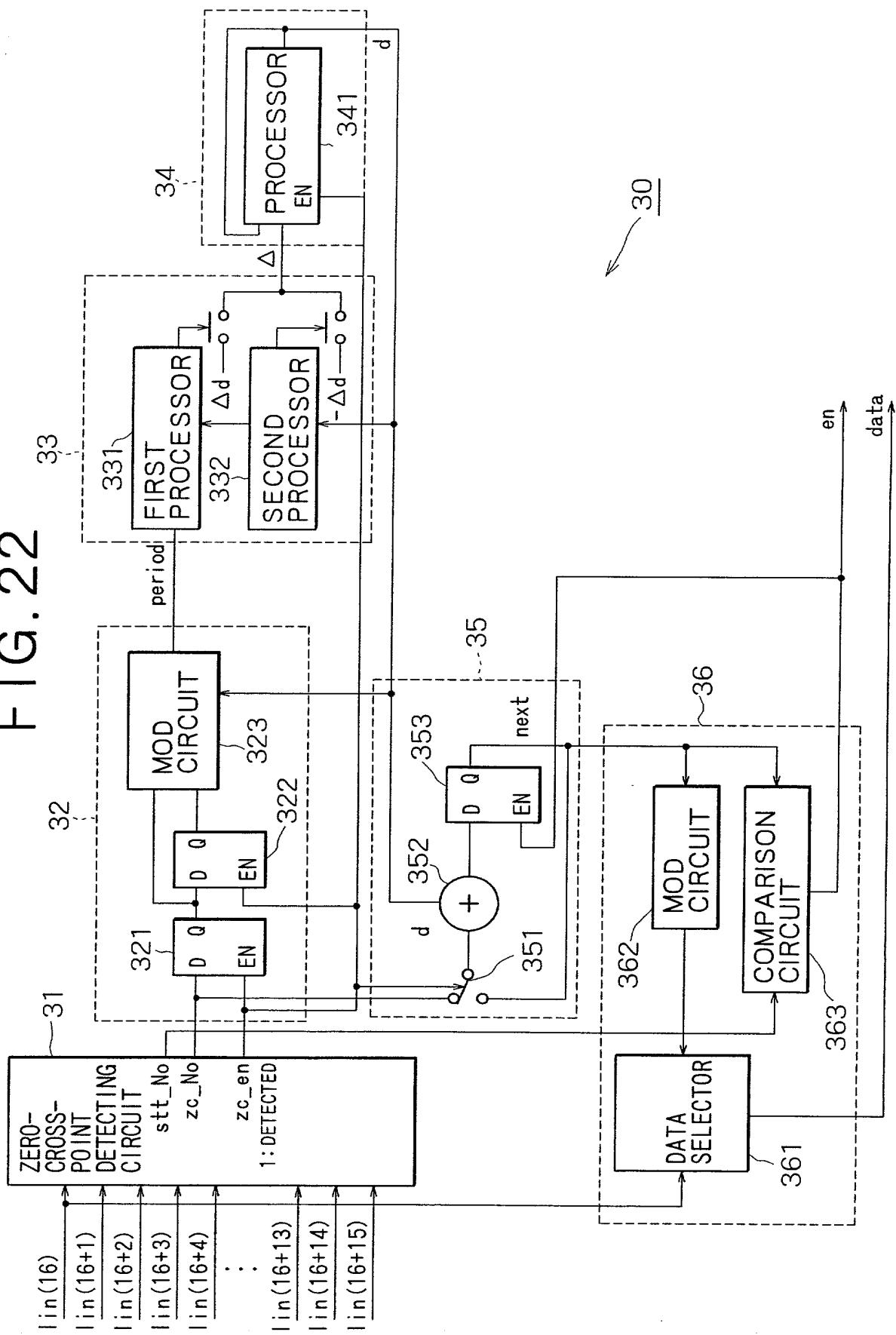


FIG. 23

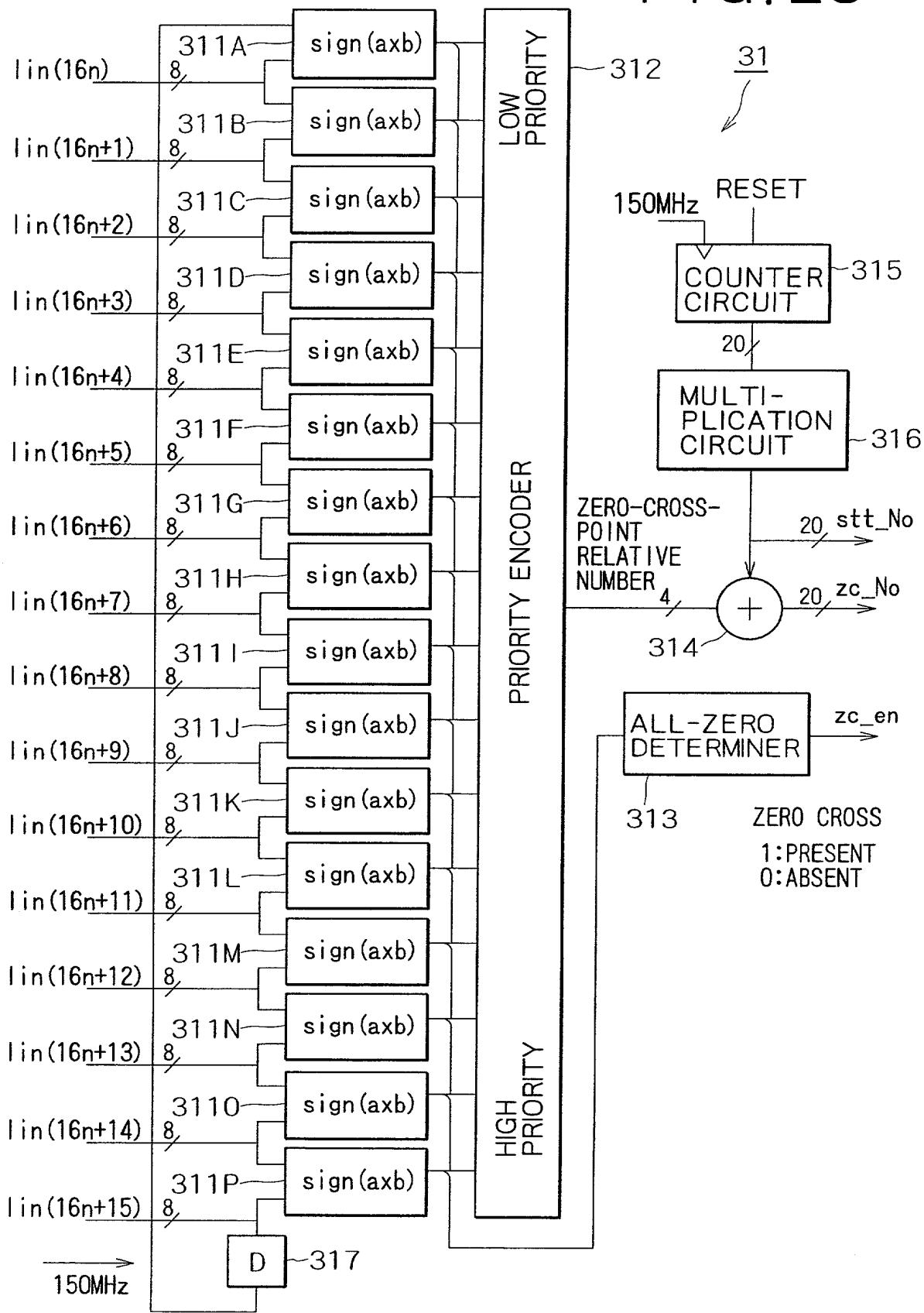


FIG. 24

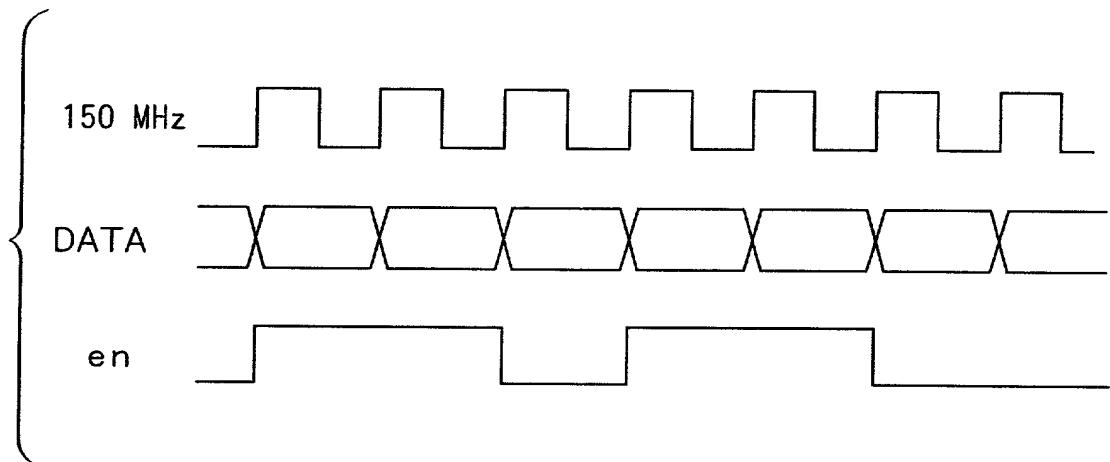


FIG. 25

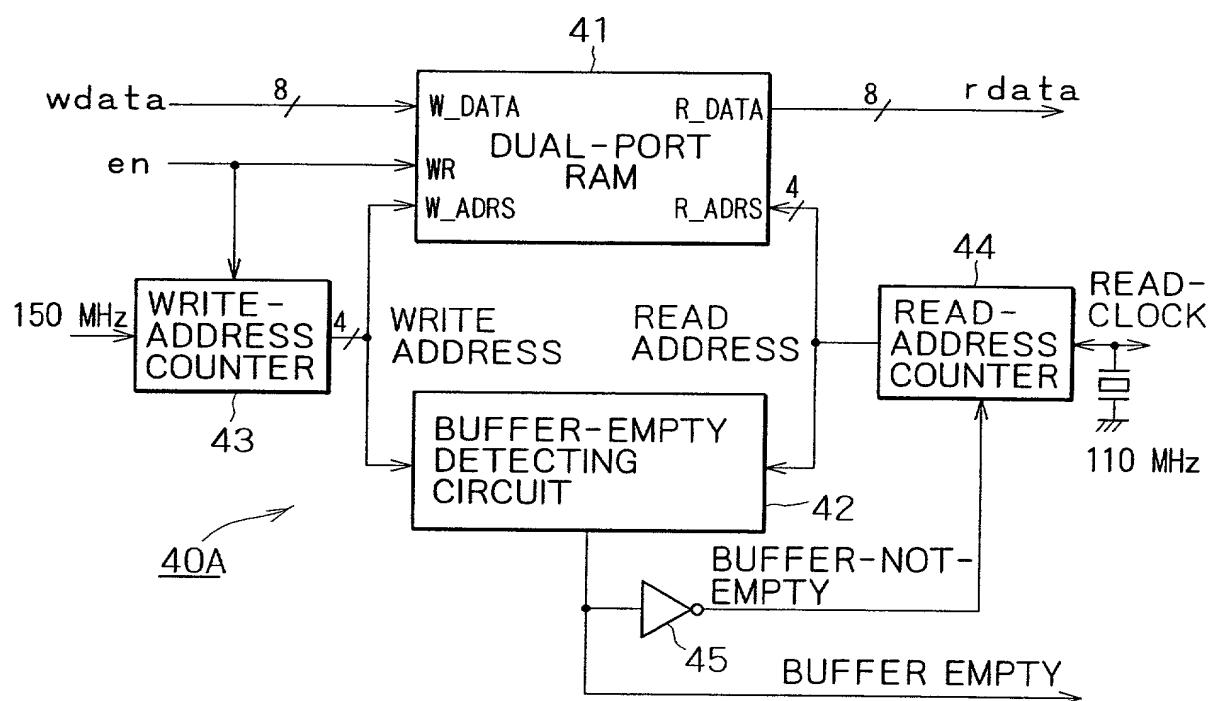


FIG. 26

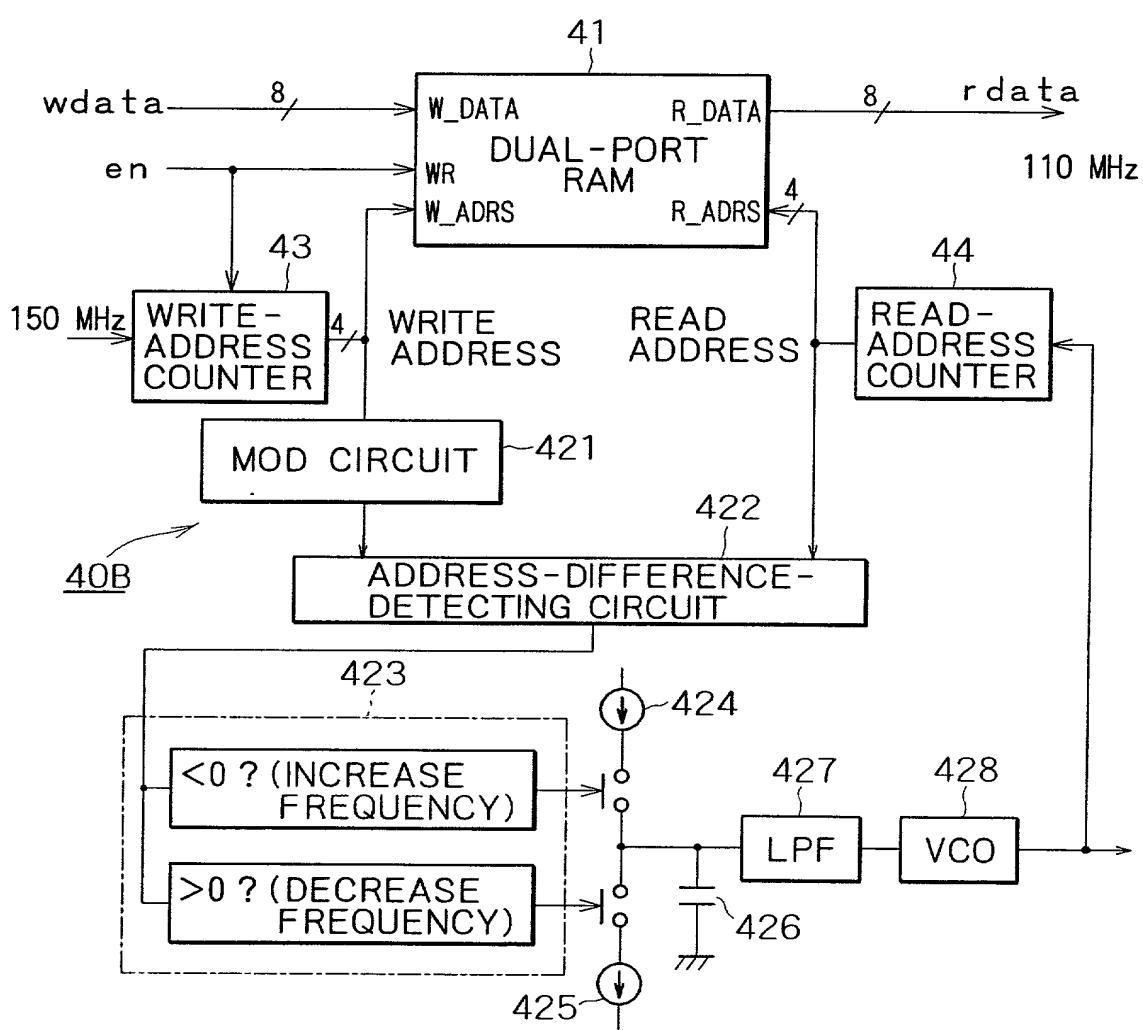


FIG. 27

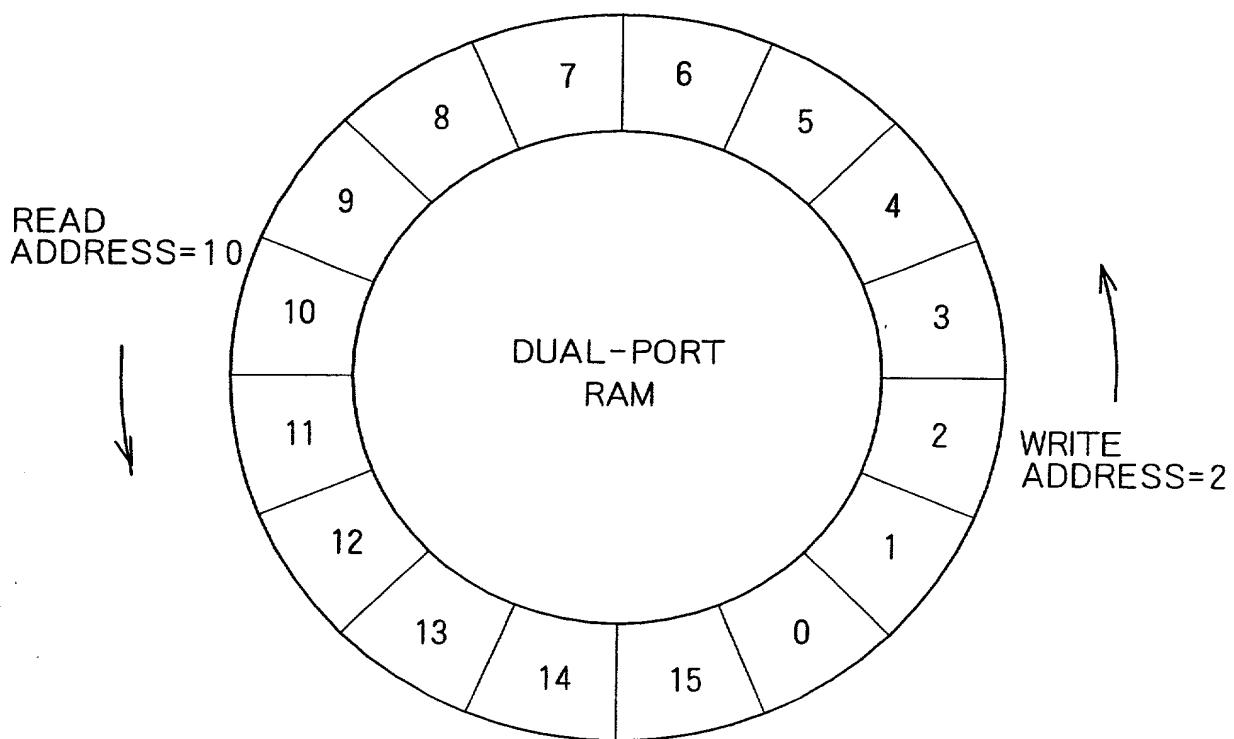


FIG. 28

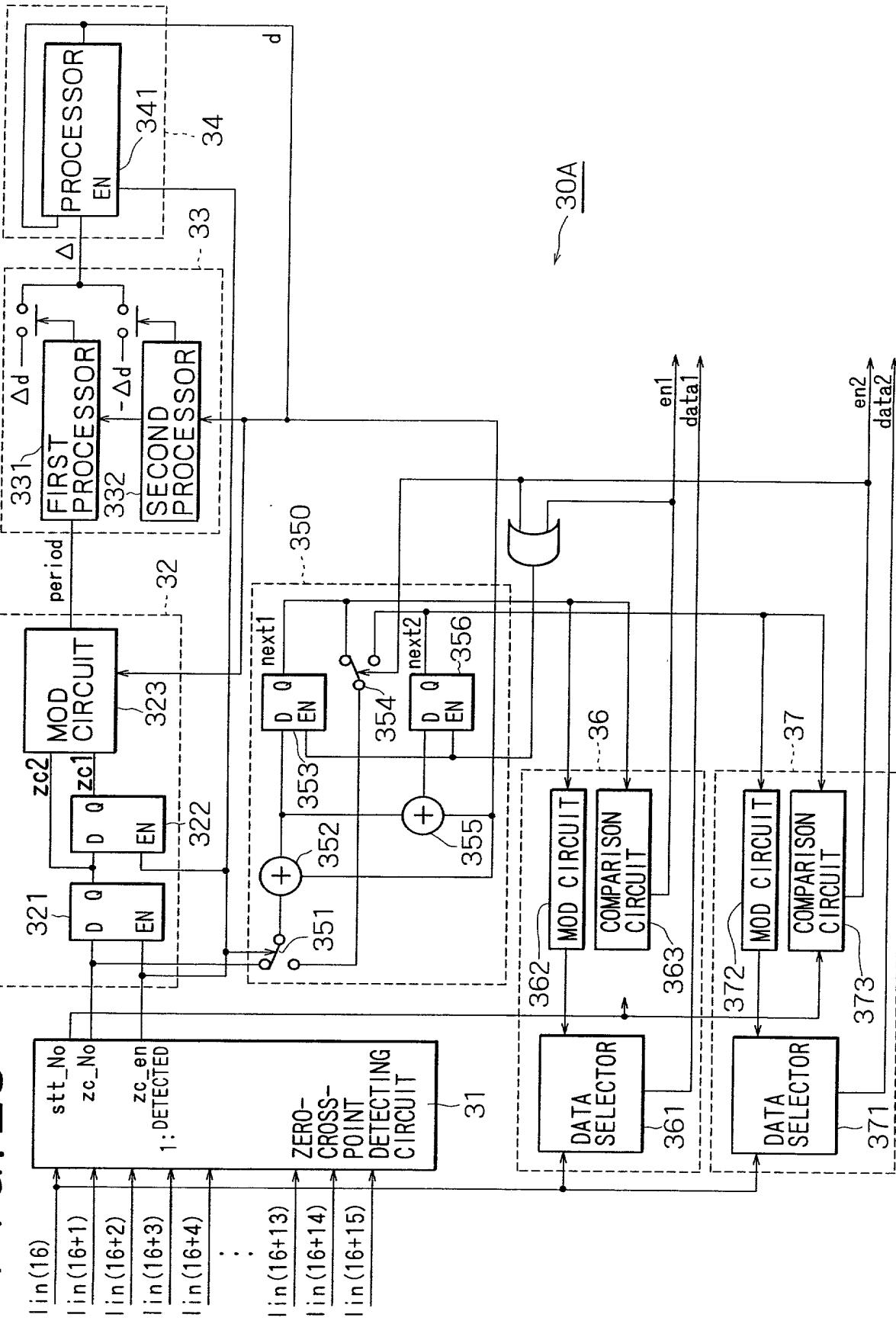


FIG. 29

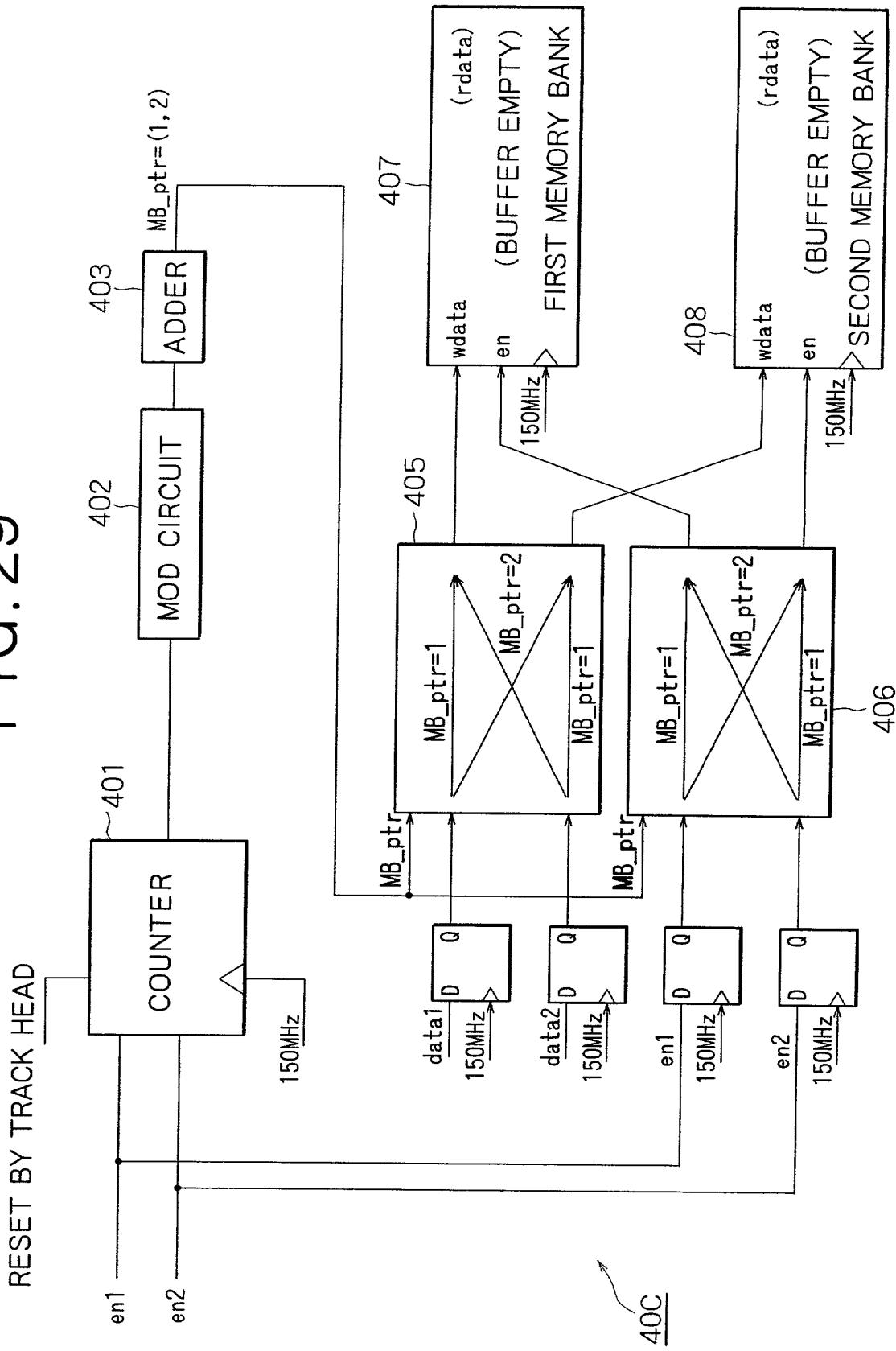


FIG. 30A

:	:
:	:
:	:
ADDRESS 5	data(10)
ADDRESS 4	data(8)
ADDRESS 3	data(6)
ADDRESS 2	data(4)
ADDRESS 1	data(2)
ADDRESS 0	data(0)

FIRST MEMORY BANK

FIG. 30B

:	:
:	:
:	:
ADDRESS 5	data(11)
ADDRESS 4	data(9)
ADDRESS 3	data(7)
ADDRESS 2	data(5)
ADDRESS 1	data(3)
ADDRESS 0	data(1)

SECOND MEMORY BANK

FIG. 31

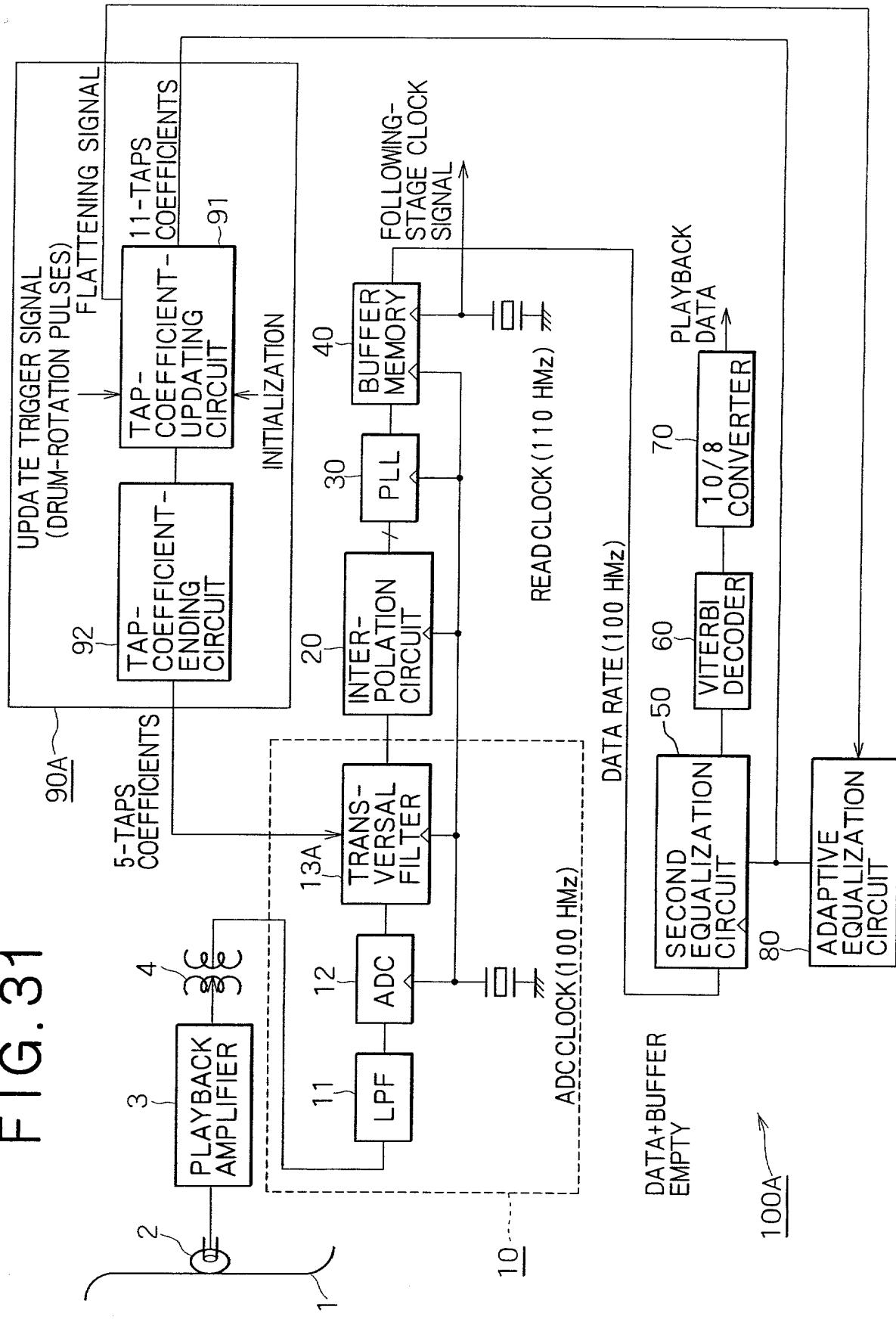


FIG. 32

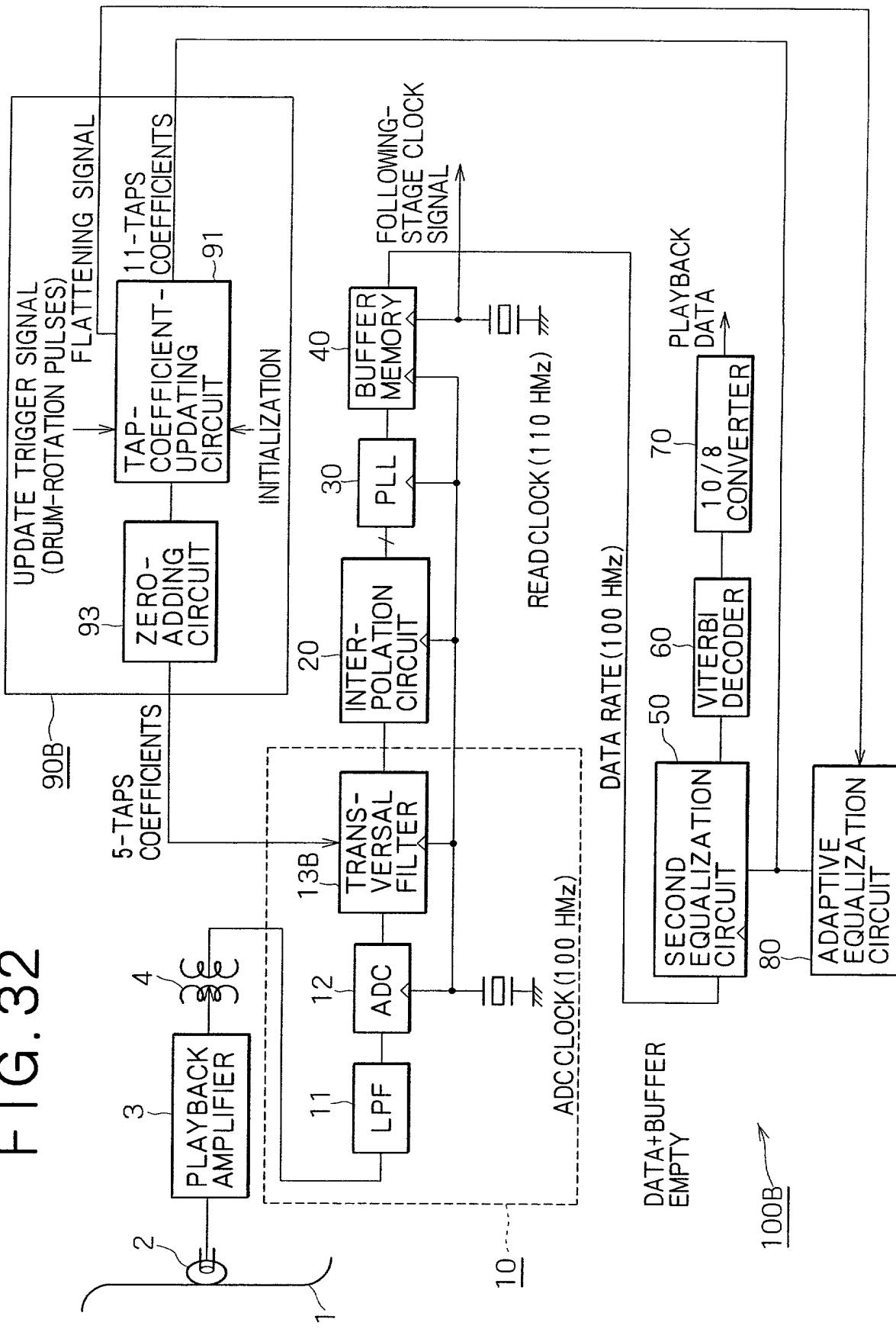


FIG. 33

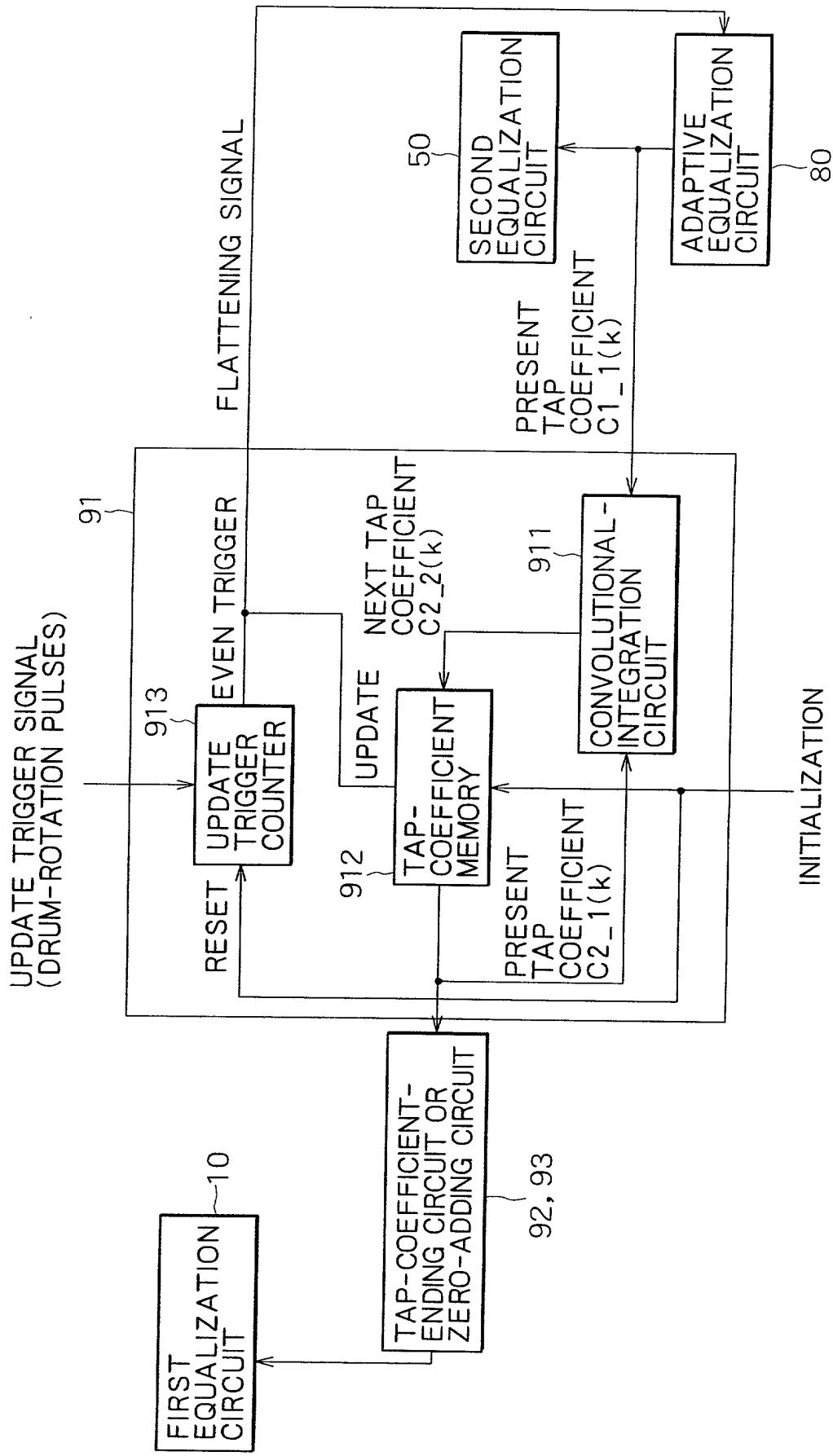


FIG. 34

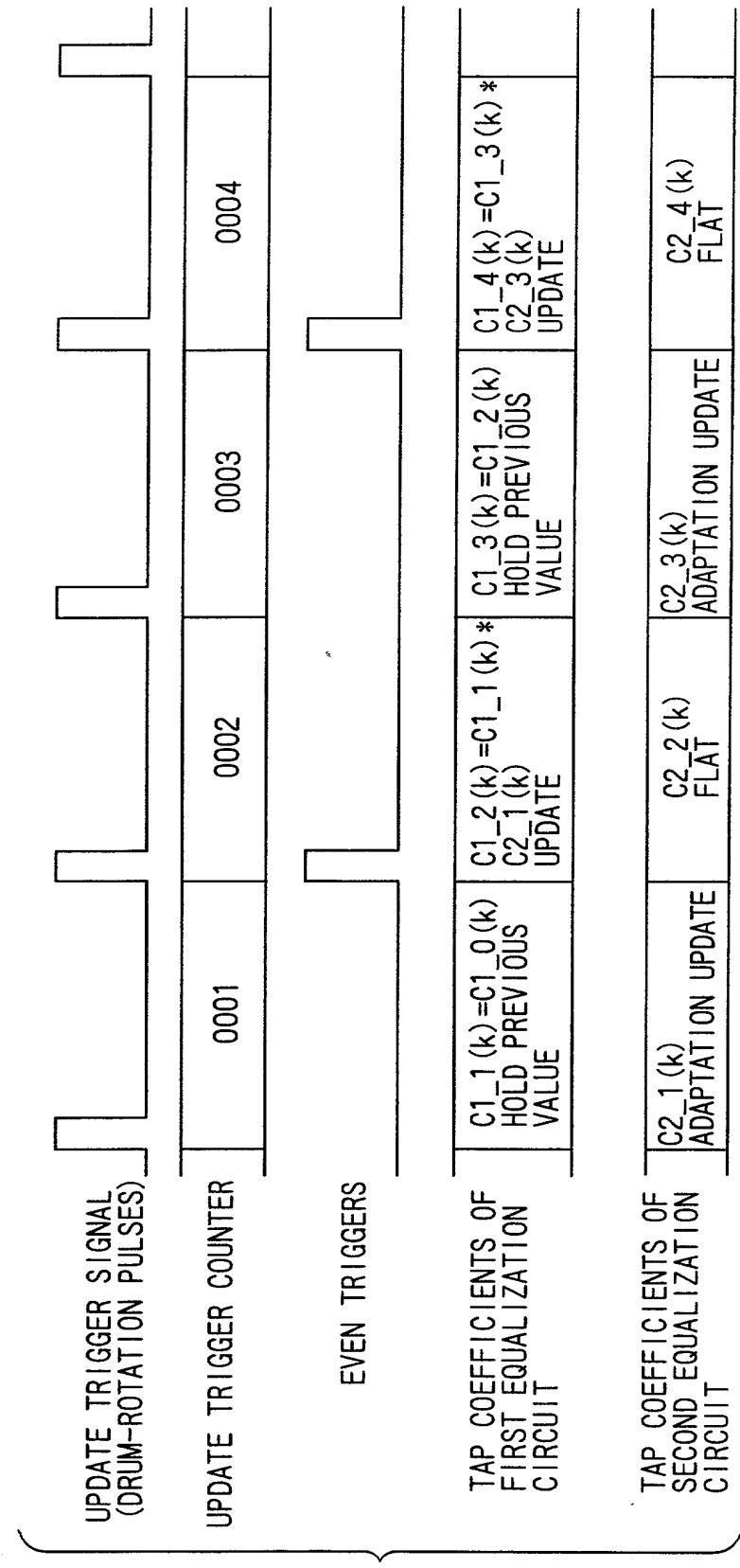


FIG. 35

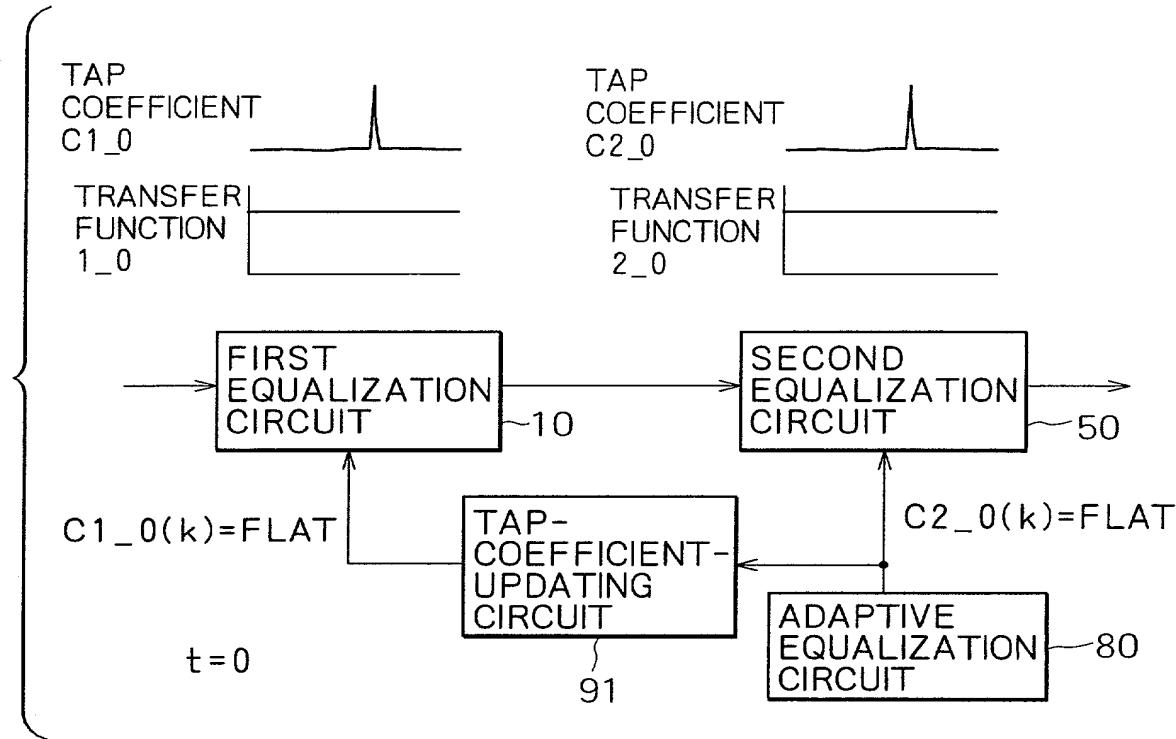


FIG. 36

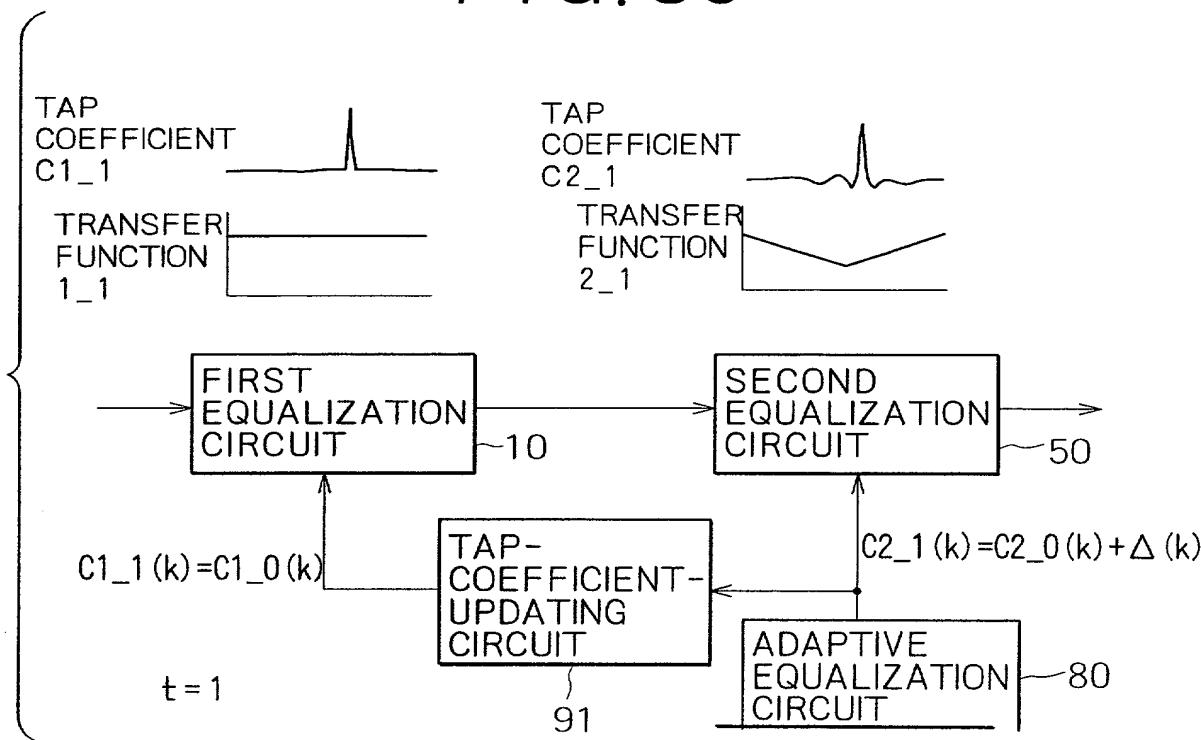


FIG. 37

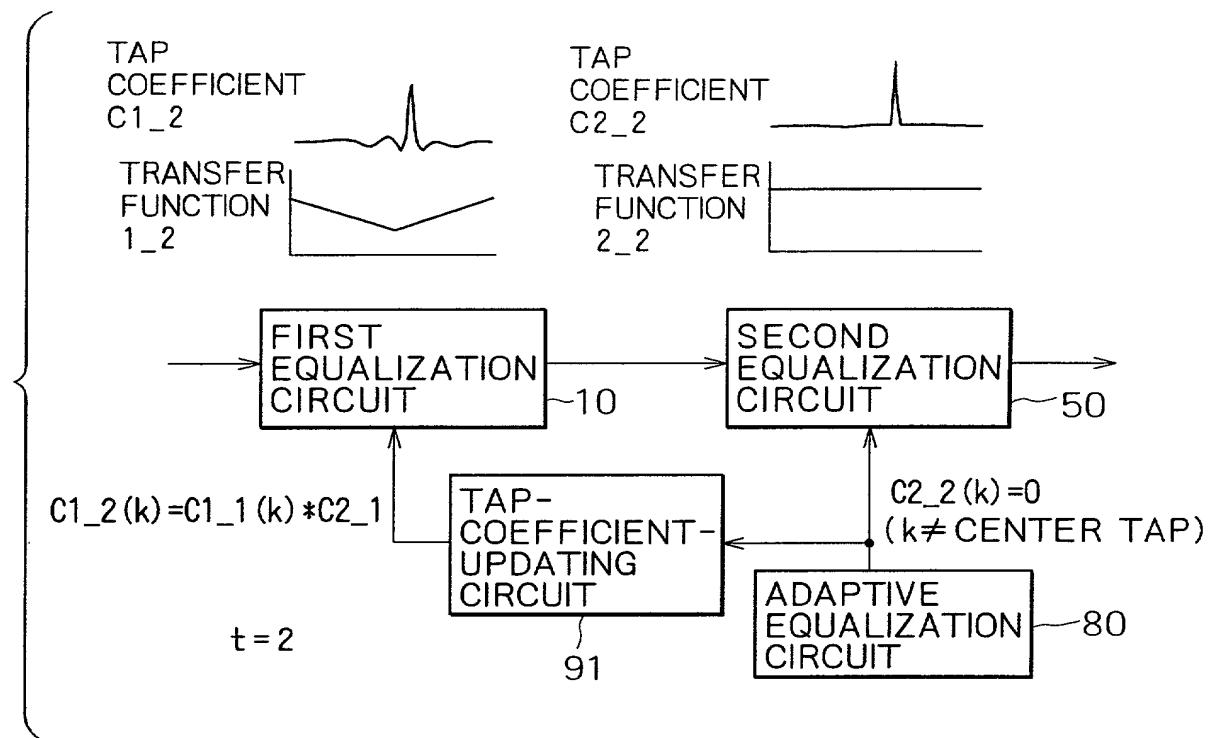
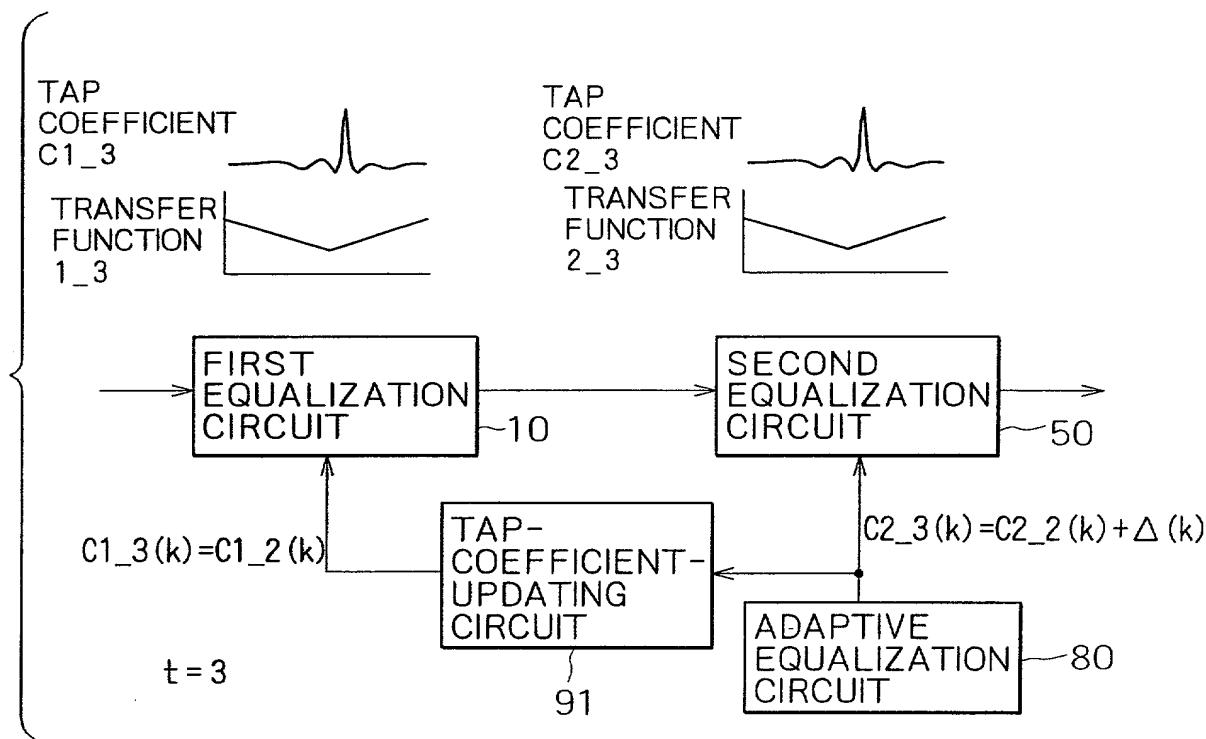


FIG. 38



E-G.39

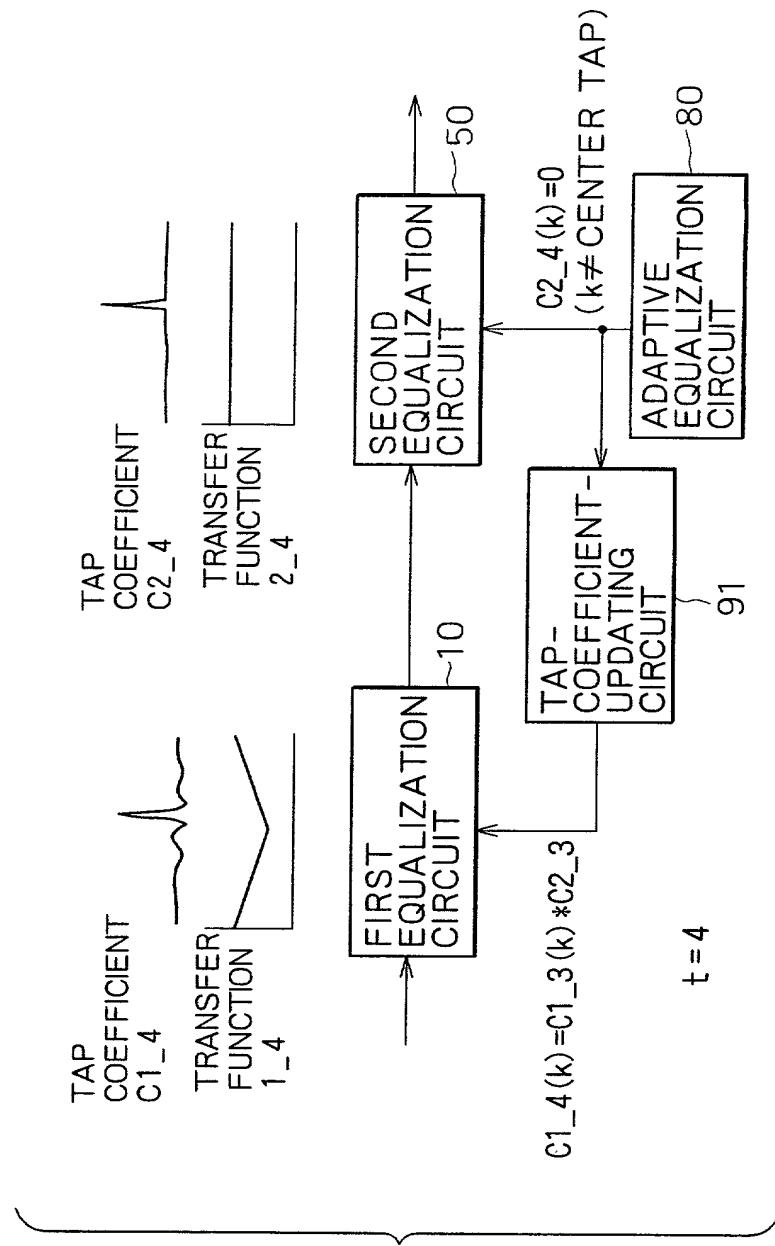


FIG. 40

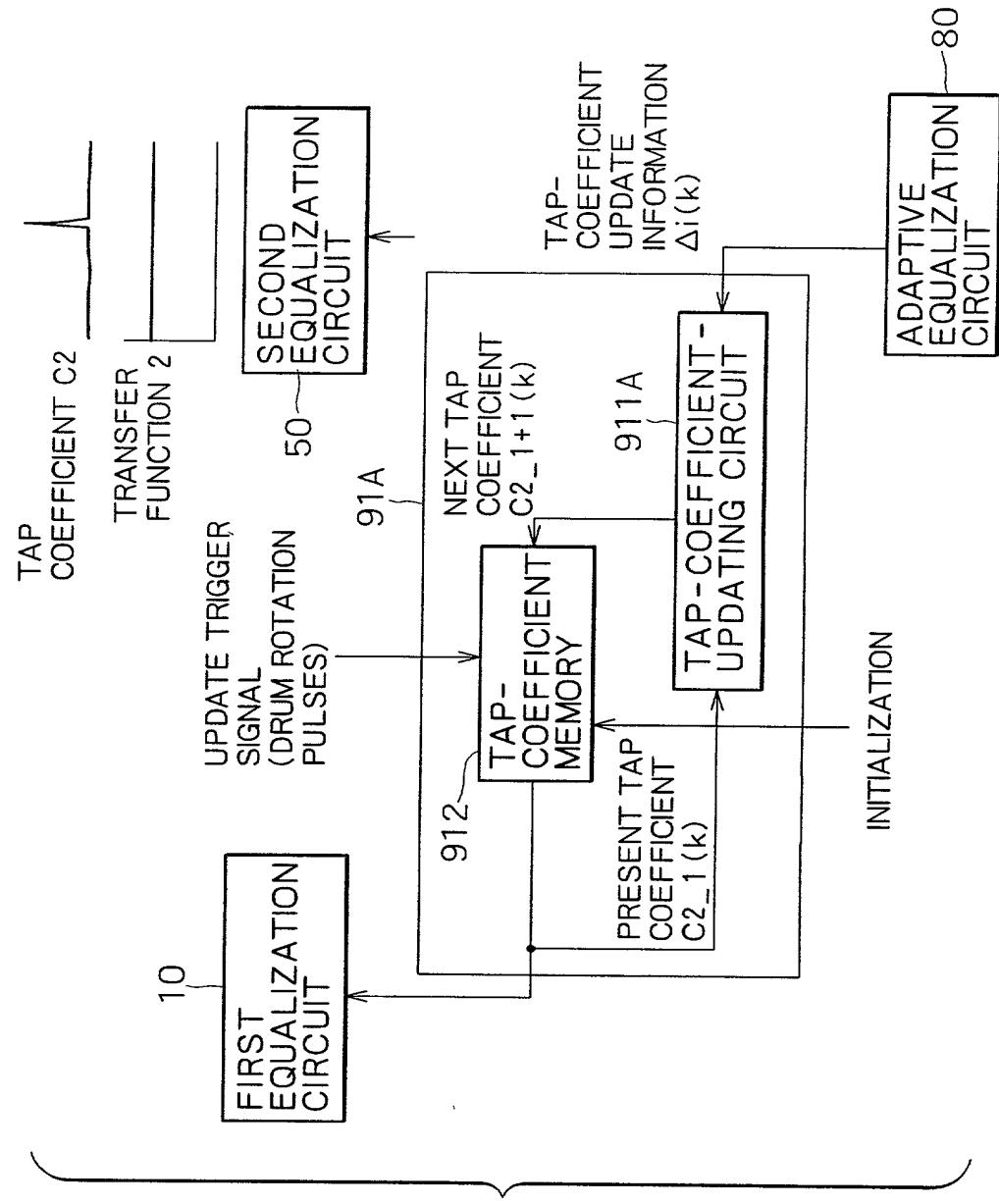


FIG. 41

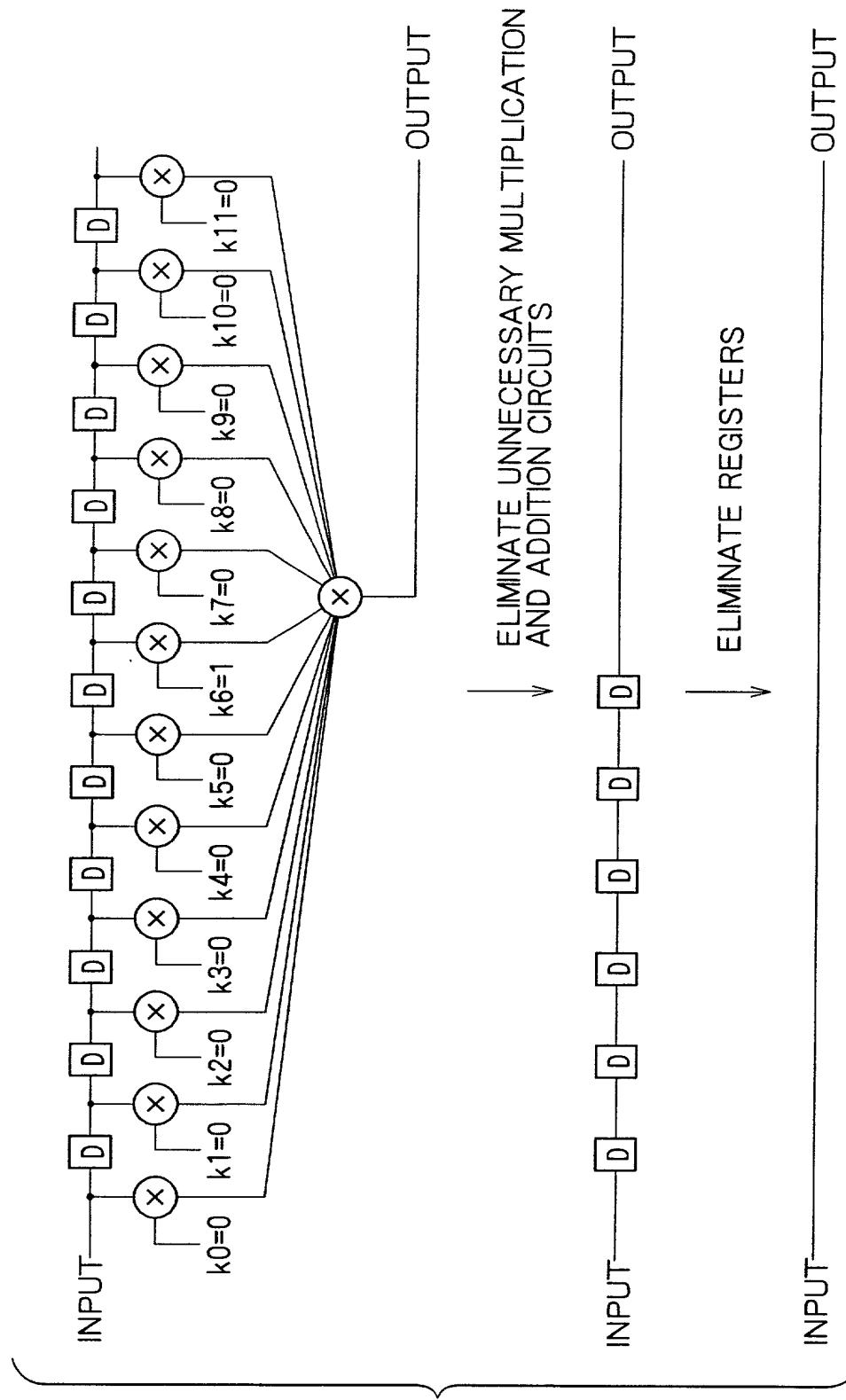


FIG. 42

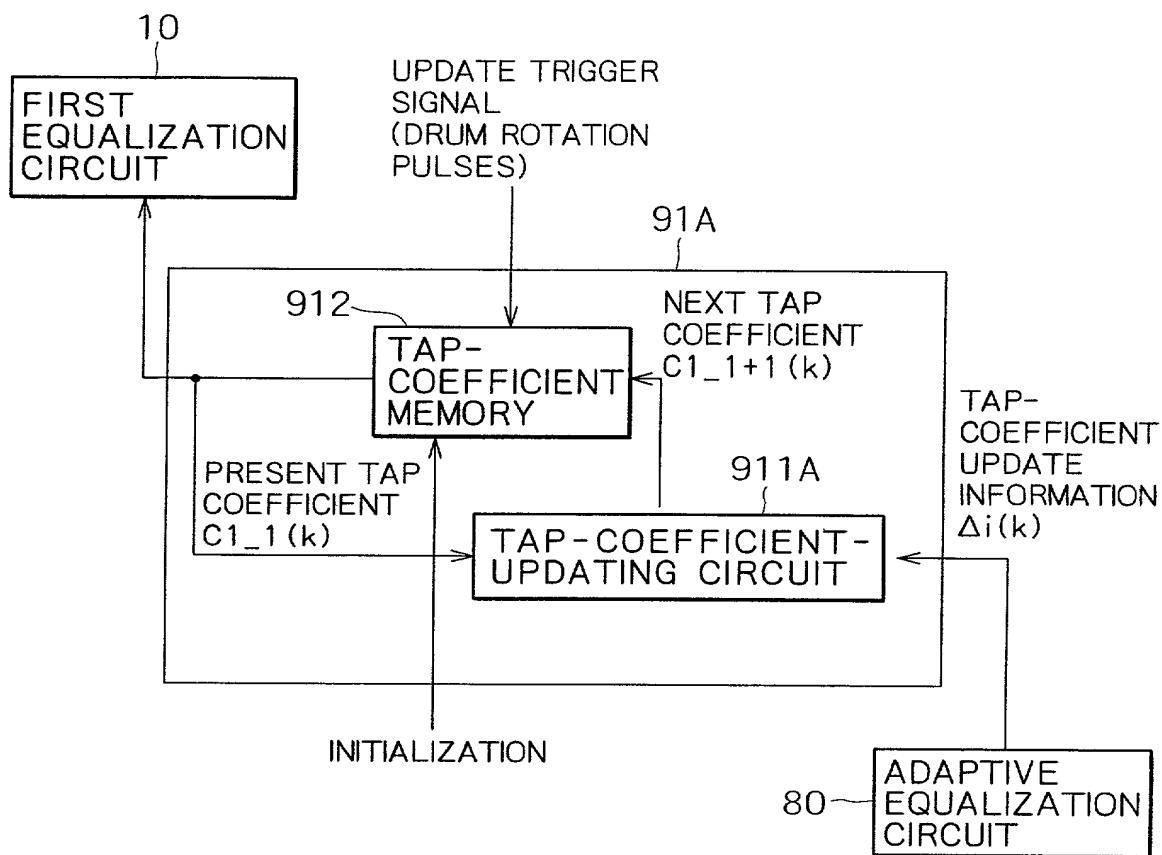


FIG. 43

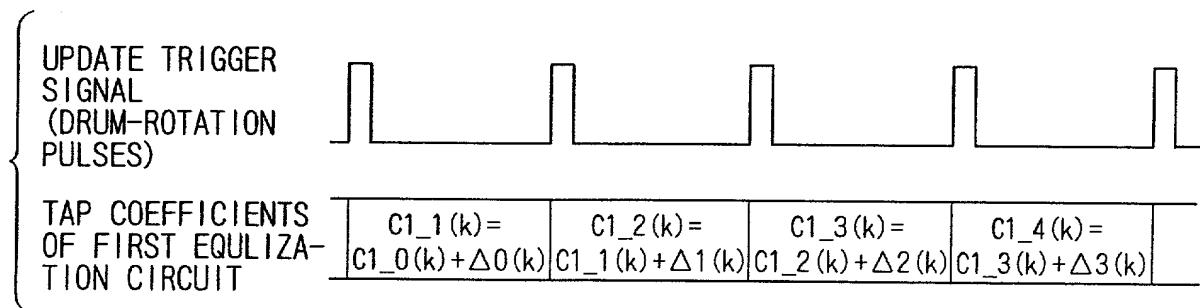


FIG. 44

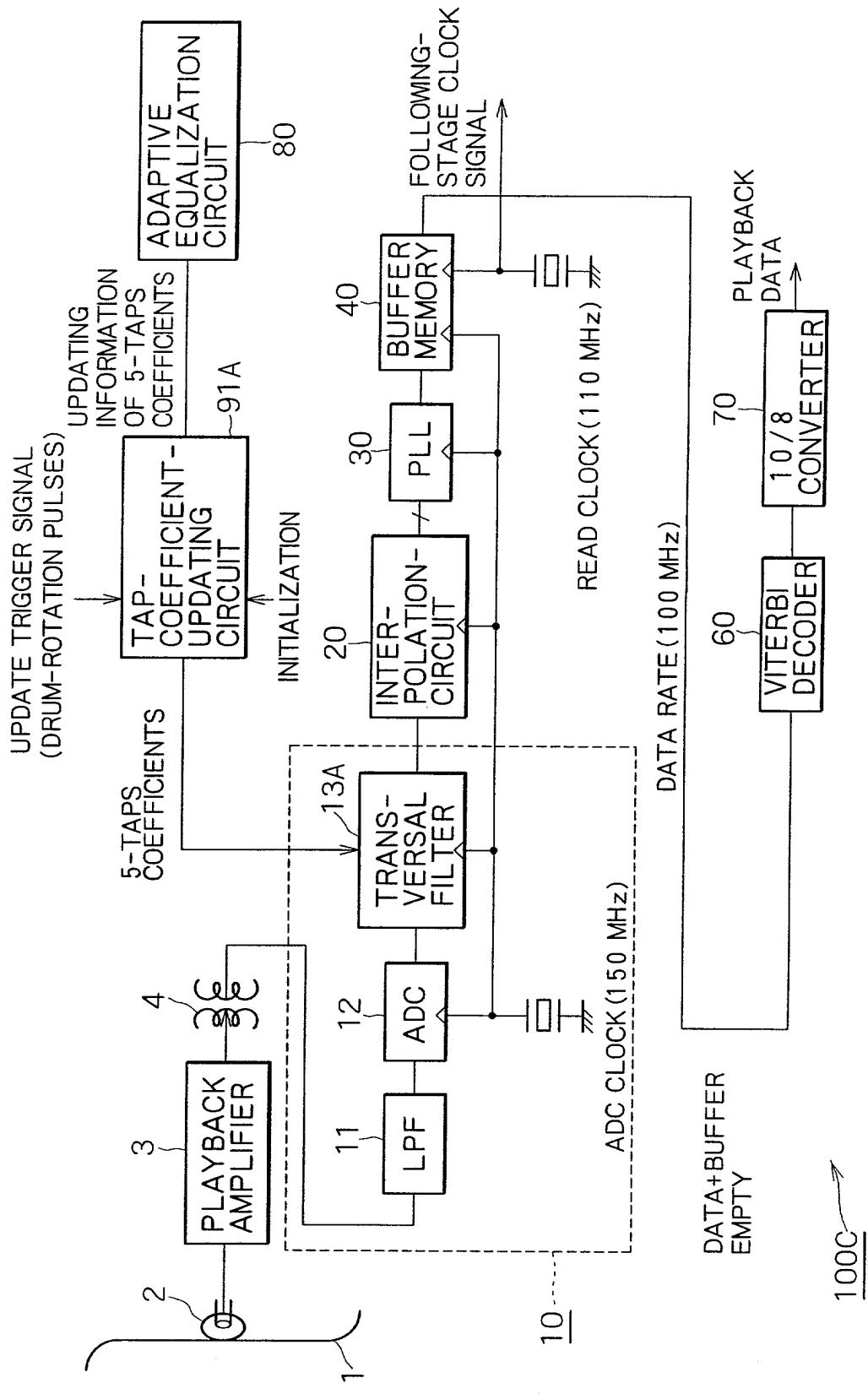


FIG. 45

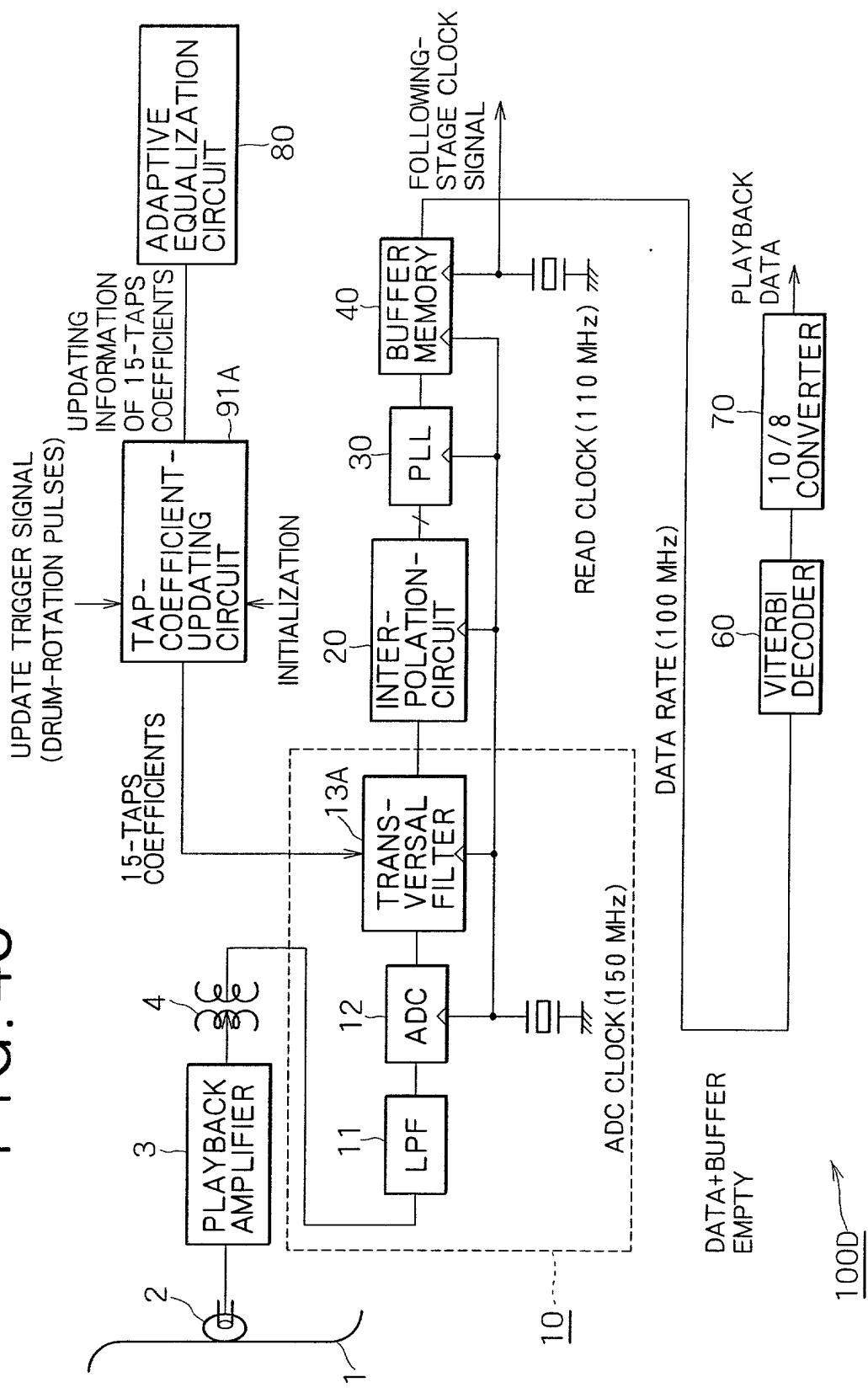


FIG. 46A

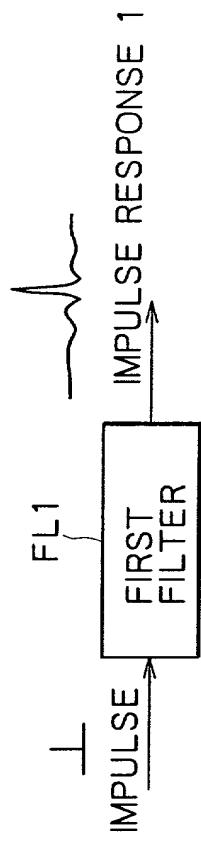


FIG. 46B

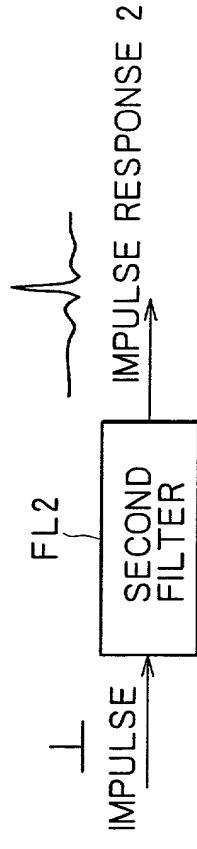


FIG. 46C

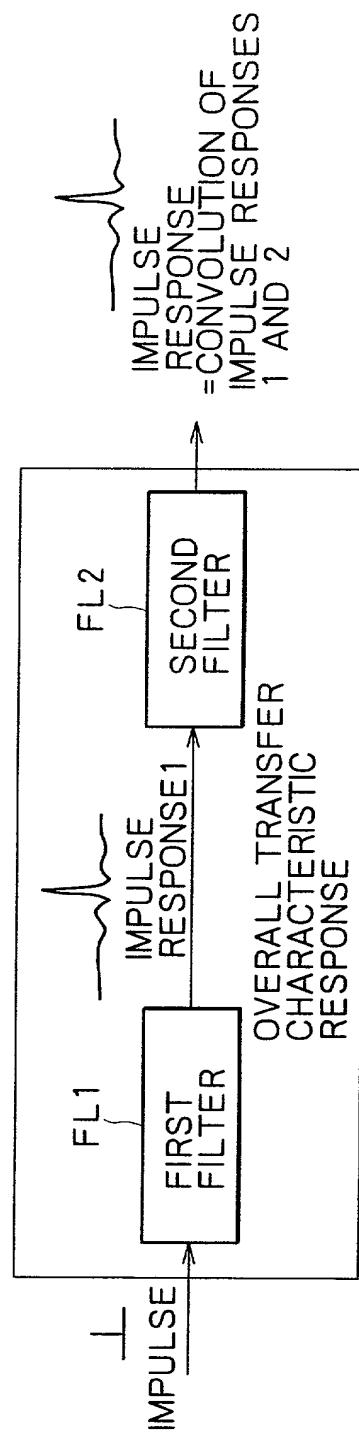


FIG. 47

TRANSFER CHARACTERISTICS OF THE 24-TAPS EQUALIZER 1 (t = i)

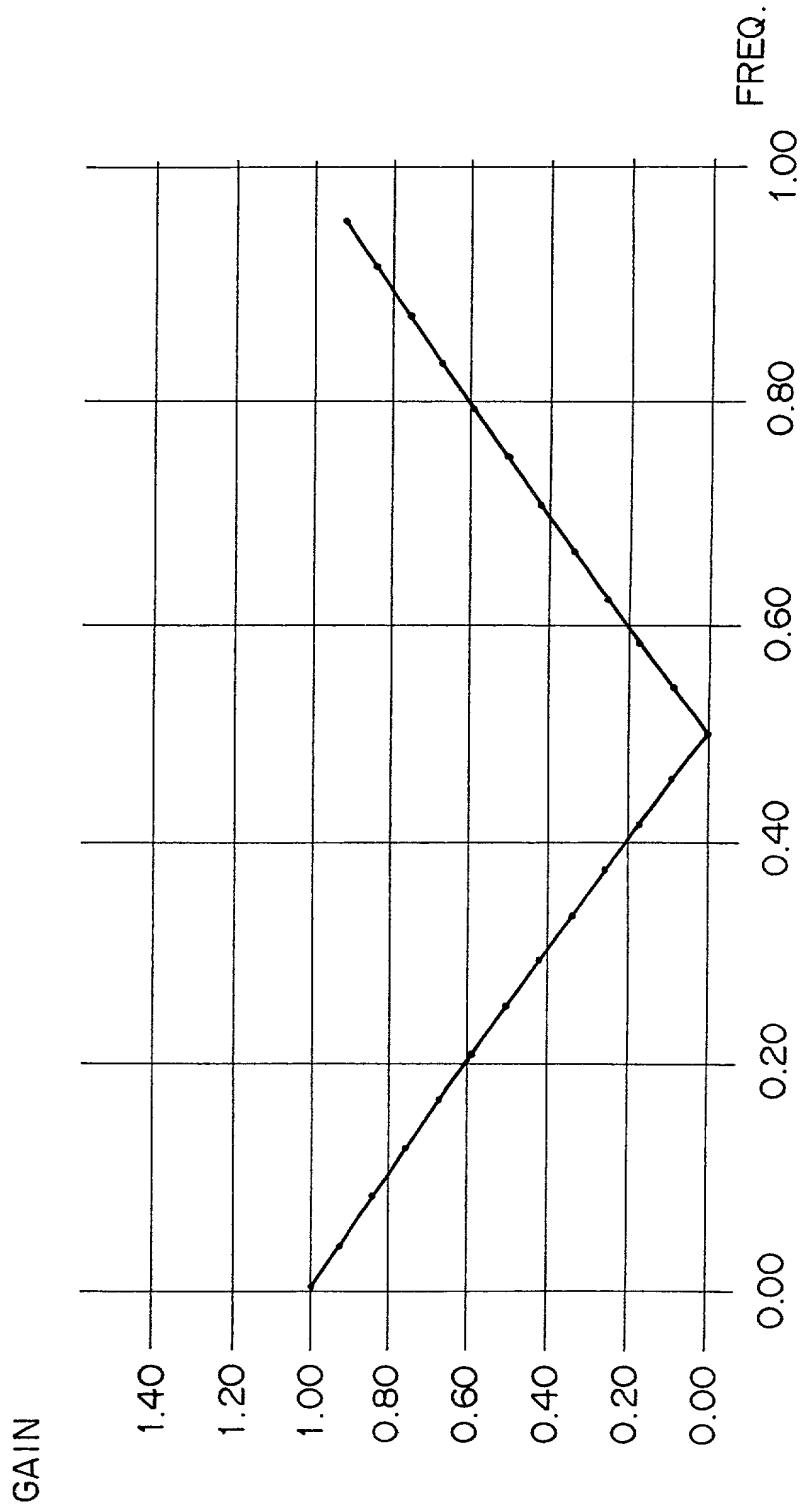


FIG. 48

EQUALIZER 1 TAP COEFFICIENT $C1_i(k)$ ($t = i$)

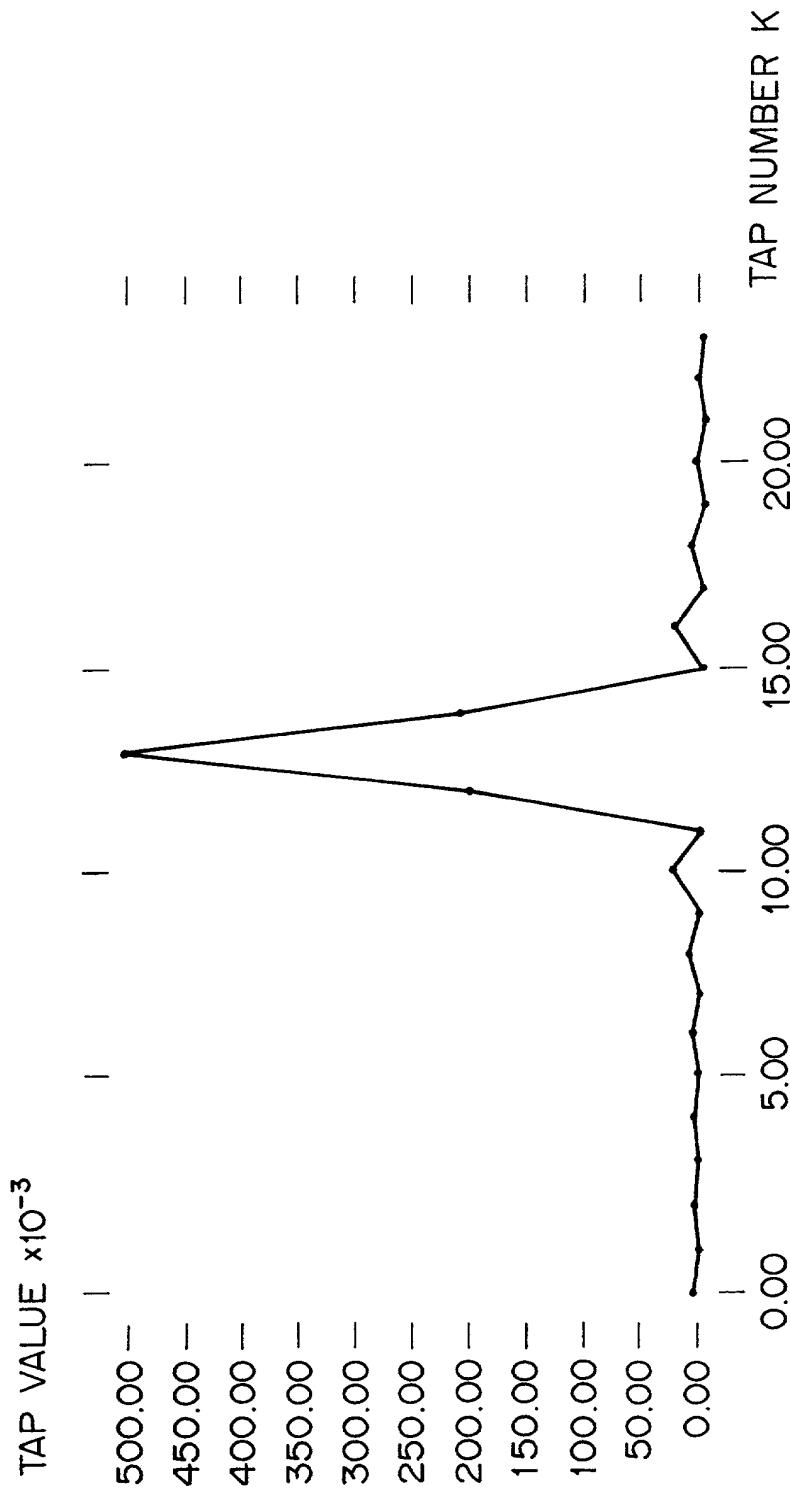
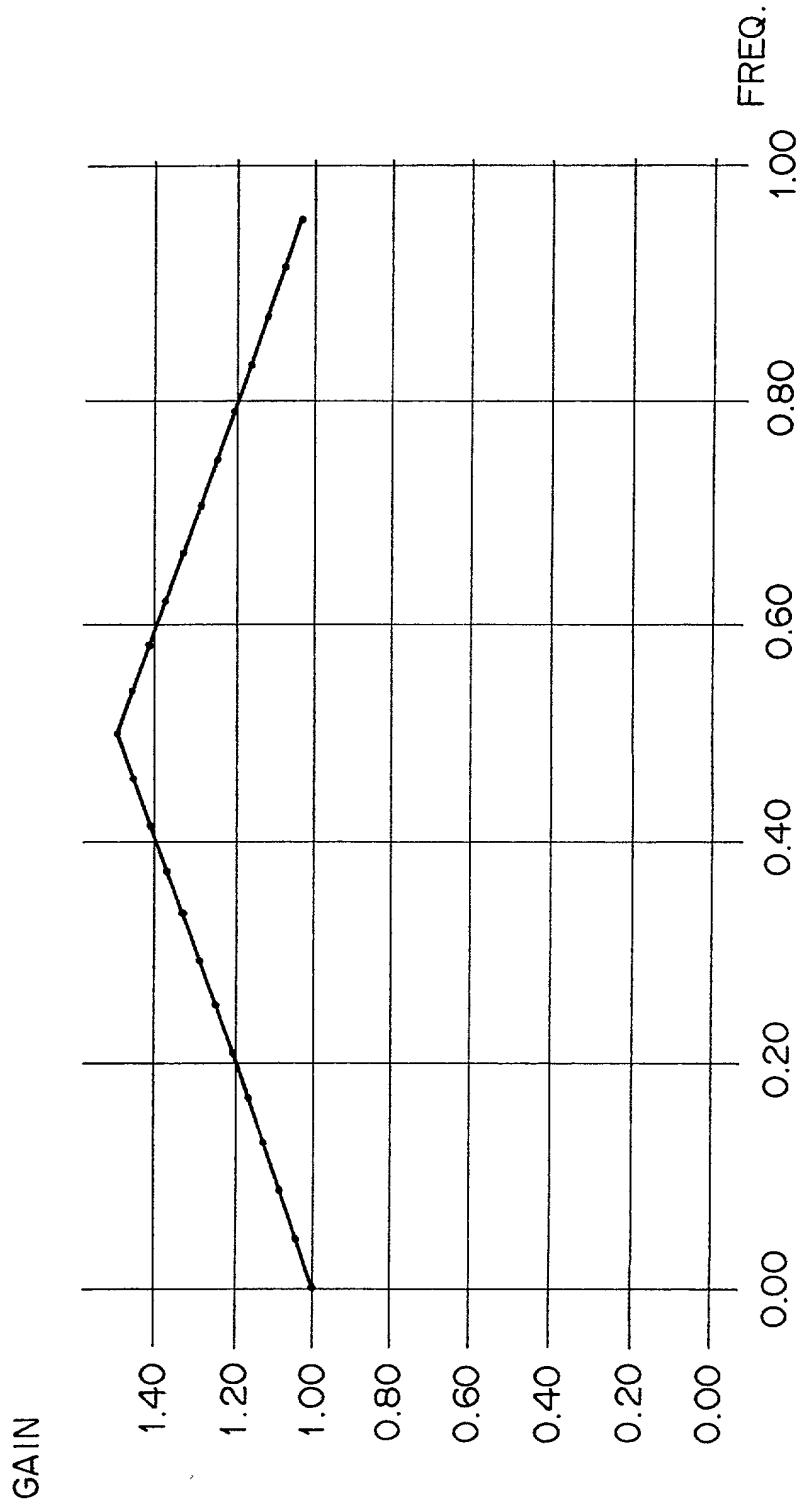


FIG. 49

TRANSFER CHARACTERISTICS OF THE 24-TAPS EQUALIZER 2 ($t = i$)



F | G. 50

EQUALIZER 2 TAP COEFFICIENT $c_{2_i}(K)$ ($t = i$)

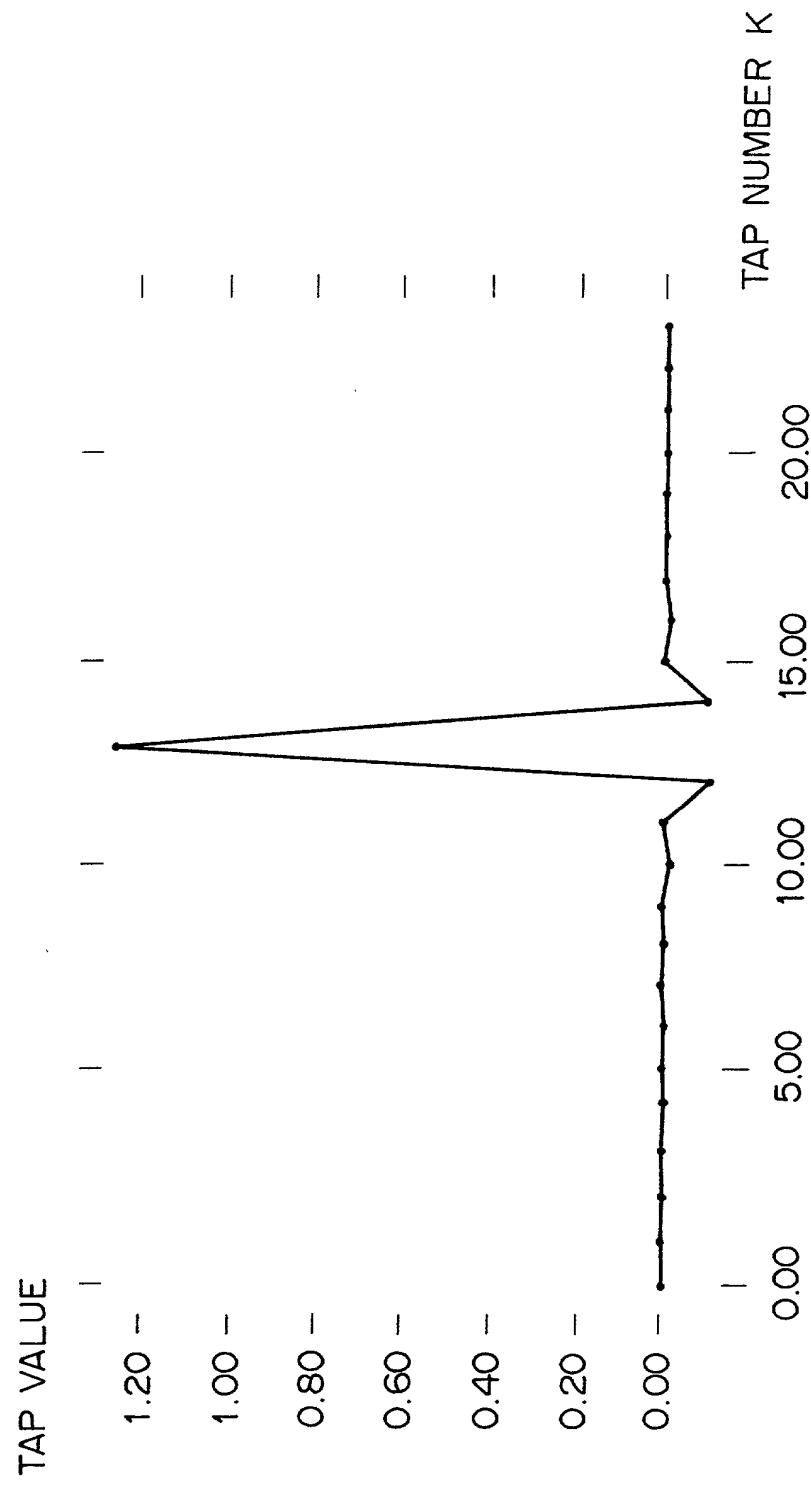


FIG. 51

EQUALIZER 1 TAP COEFFICIENT $C_{2-i+1}(K)$ ($t = i + 1$)

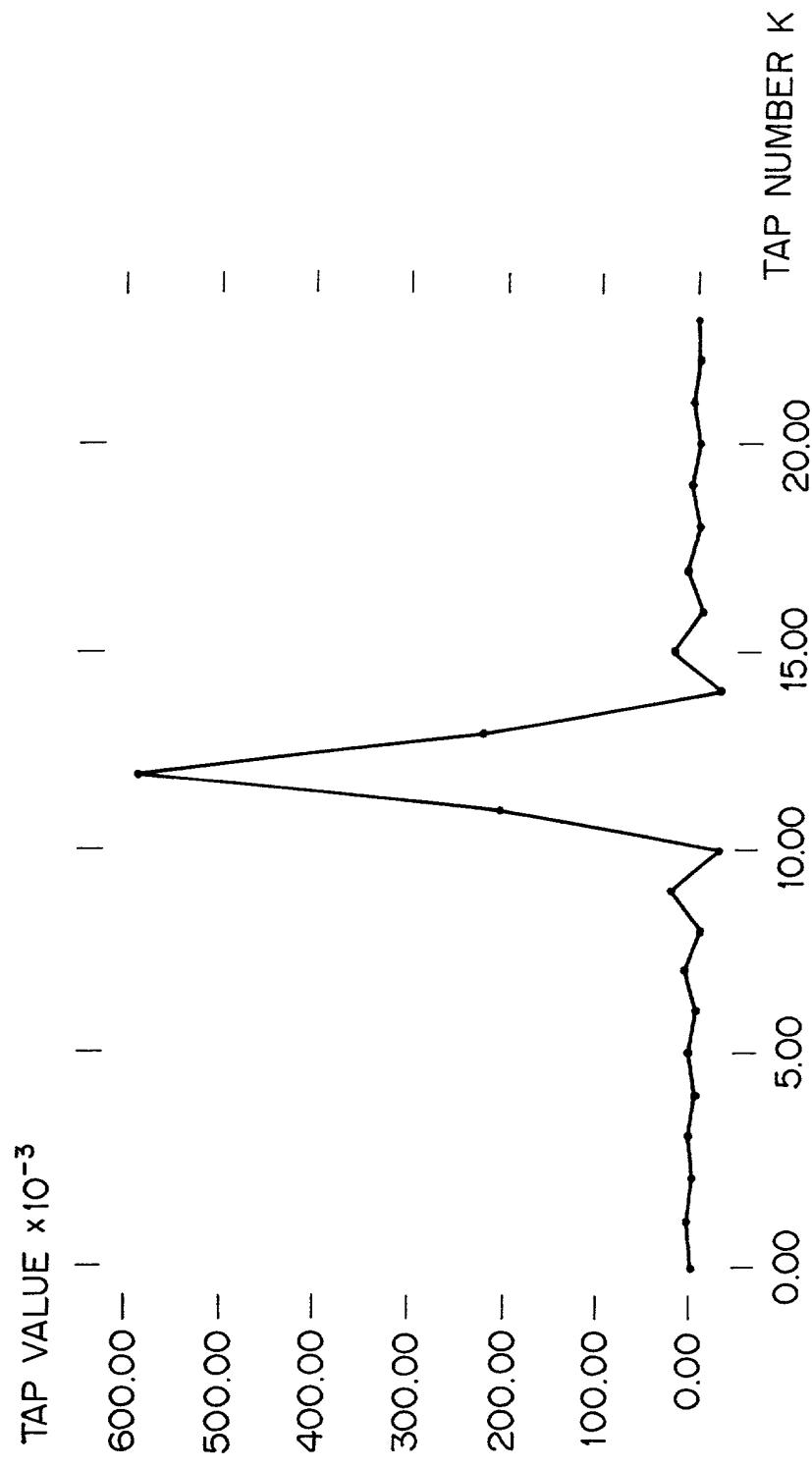


FIG. 52

TRANSFER CHARACTERISTICS OF THE 24-TAPS EQUALIZER 1 ($t = i + 1$)

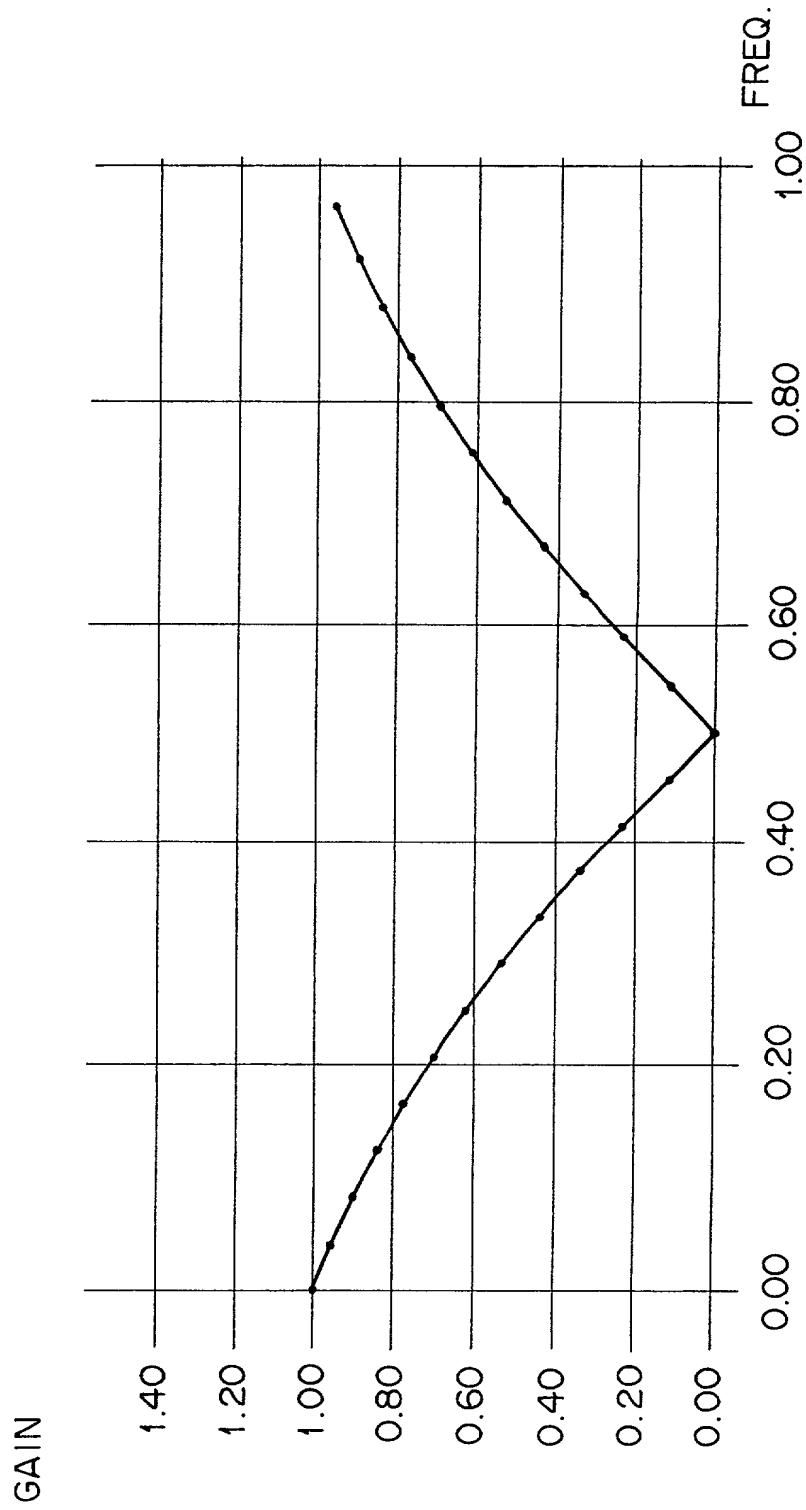


FIG. 53

TRANSFER CHARACTERISTICS OF THE 24-TAPS EQUALIZER 1 ($t = i + 1$)

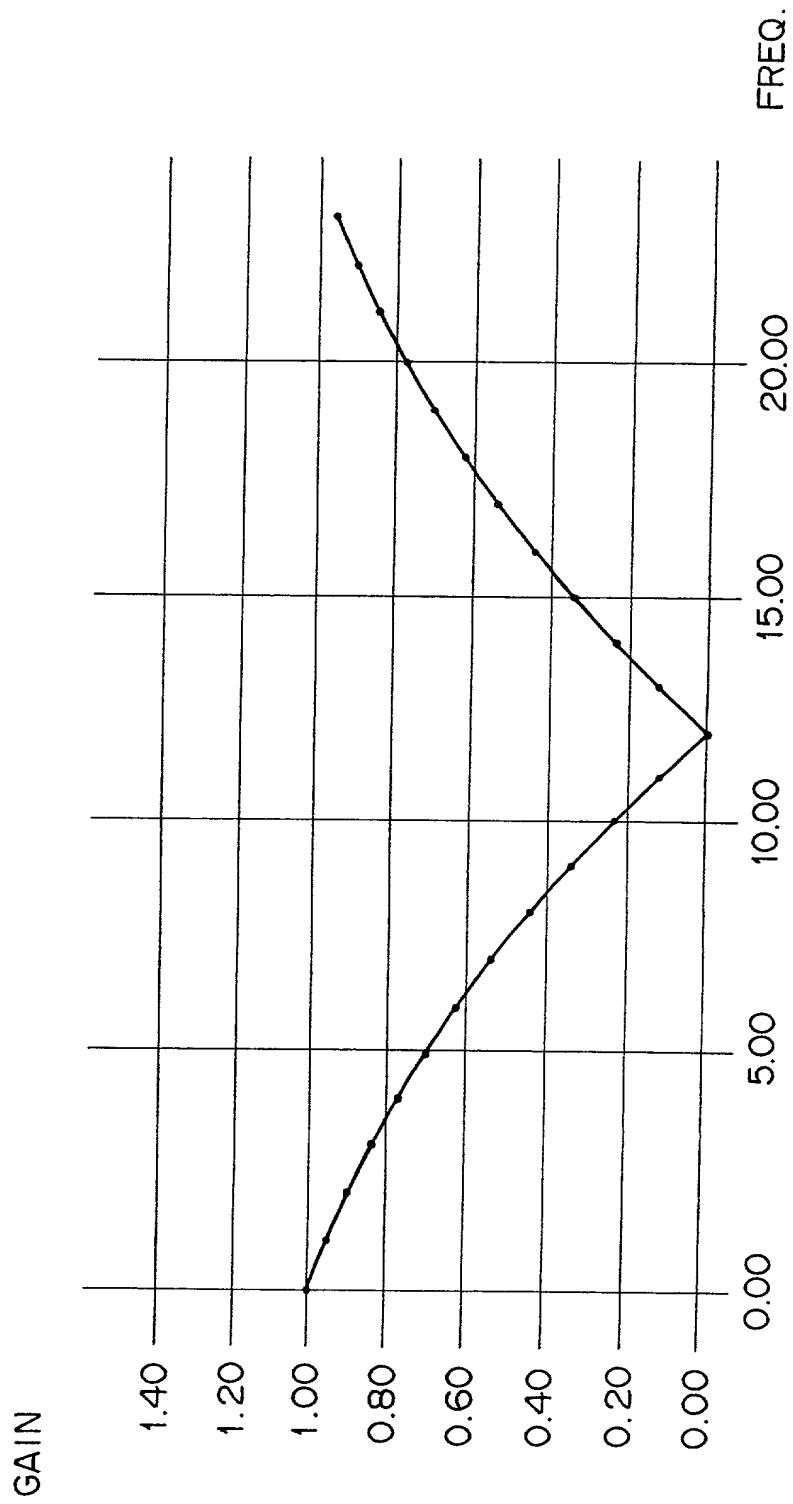


FIG. 54

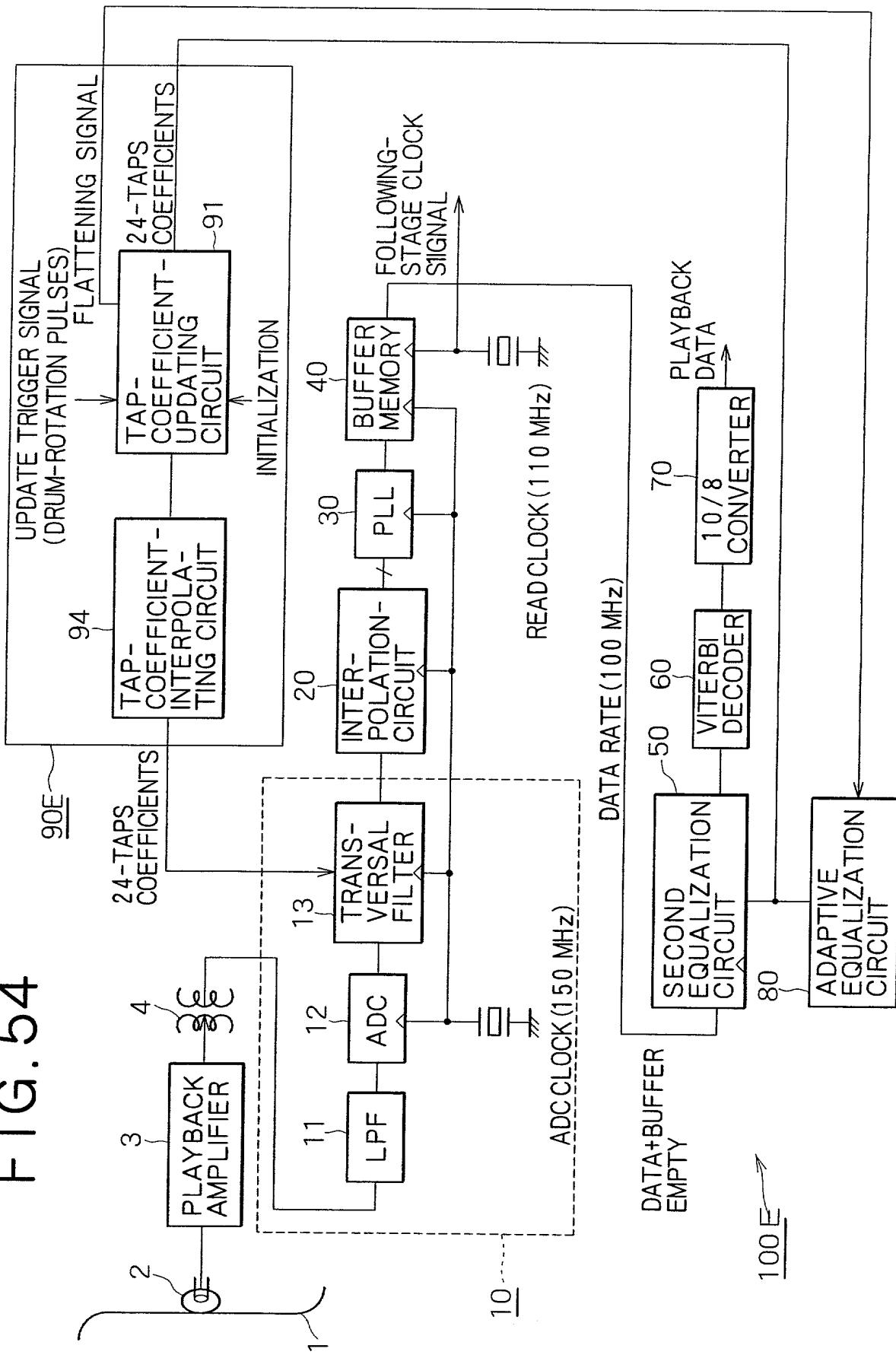


FIG. 55

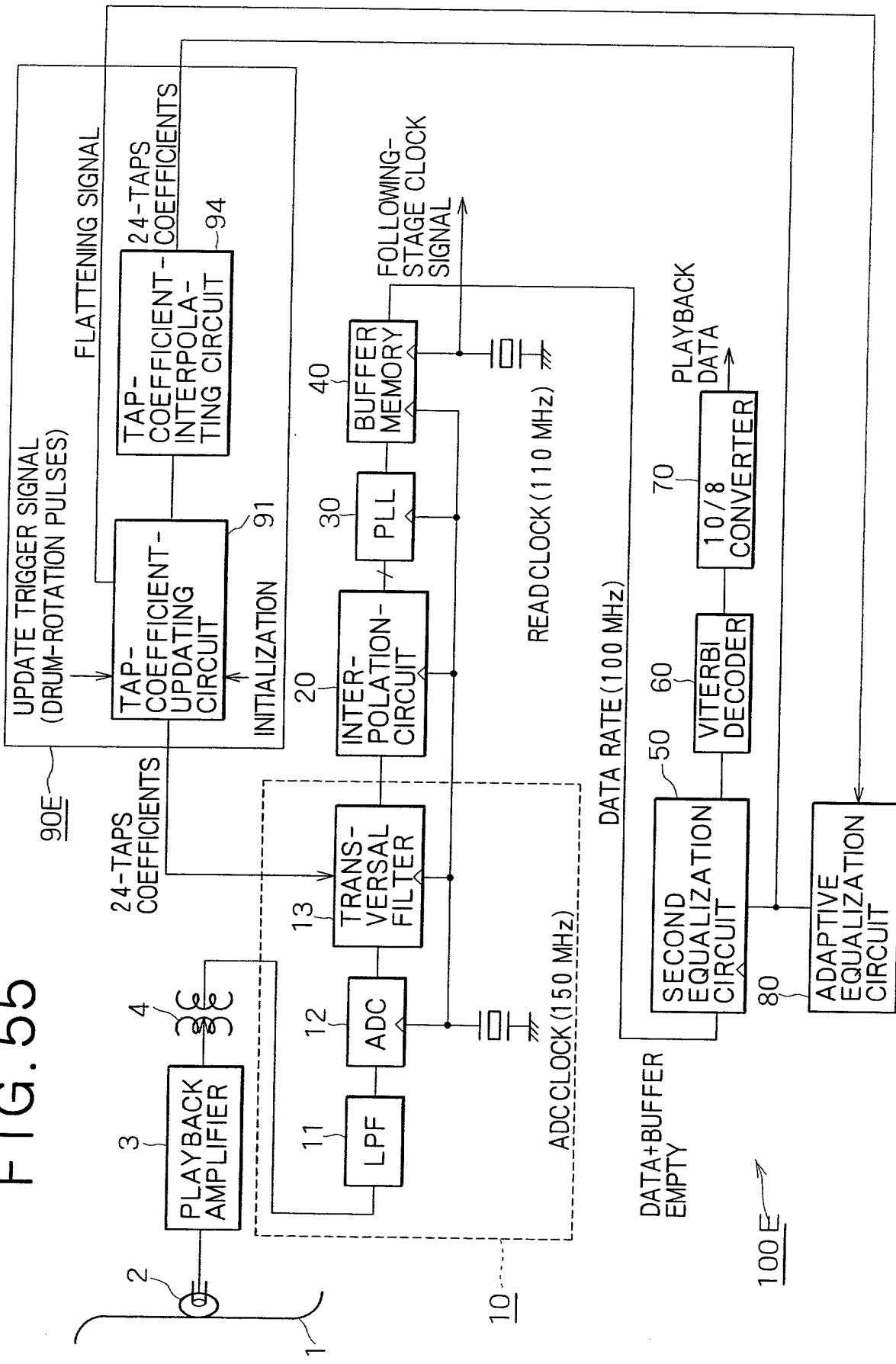


FIG. 56

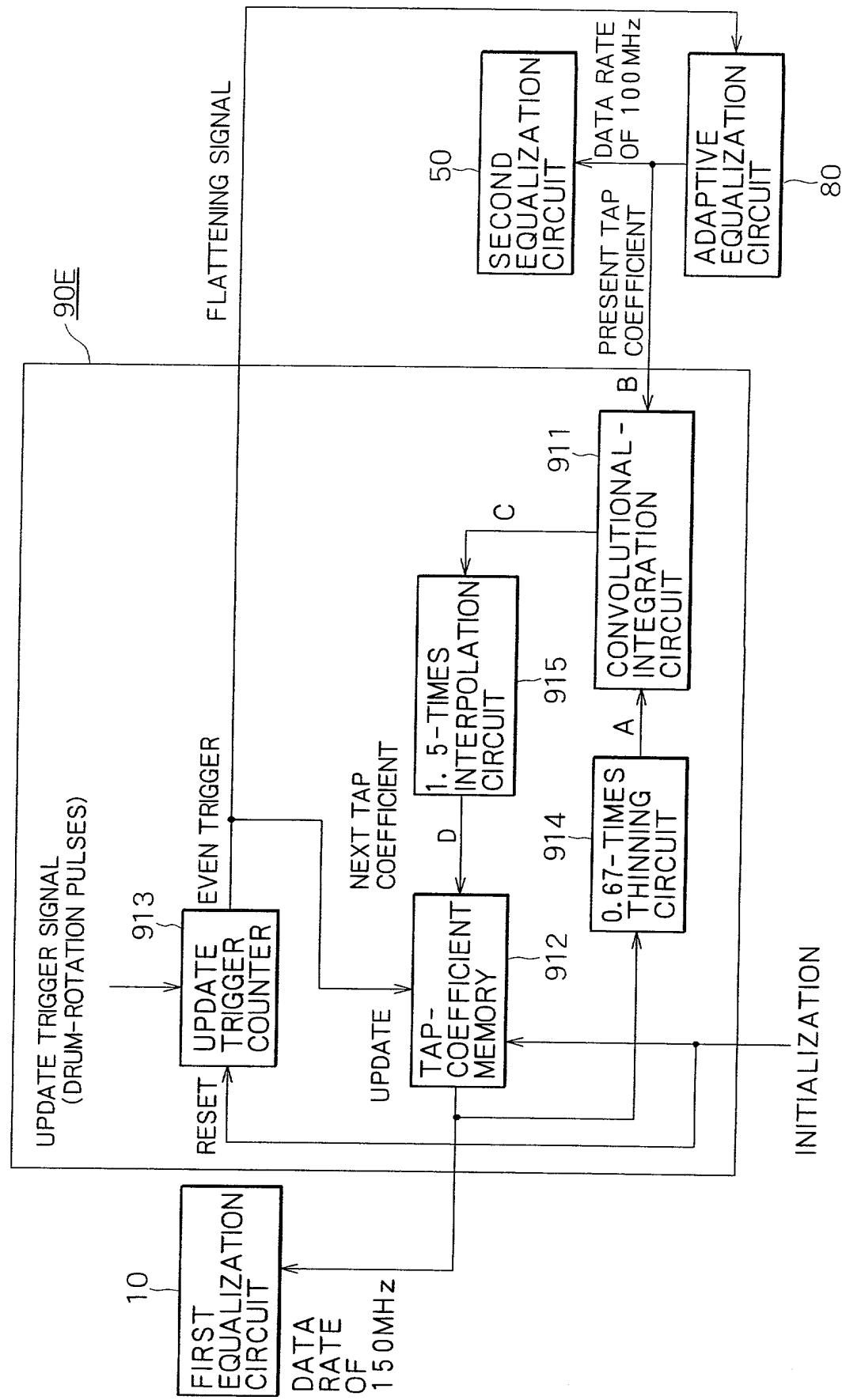


FIG. 57

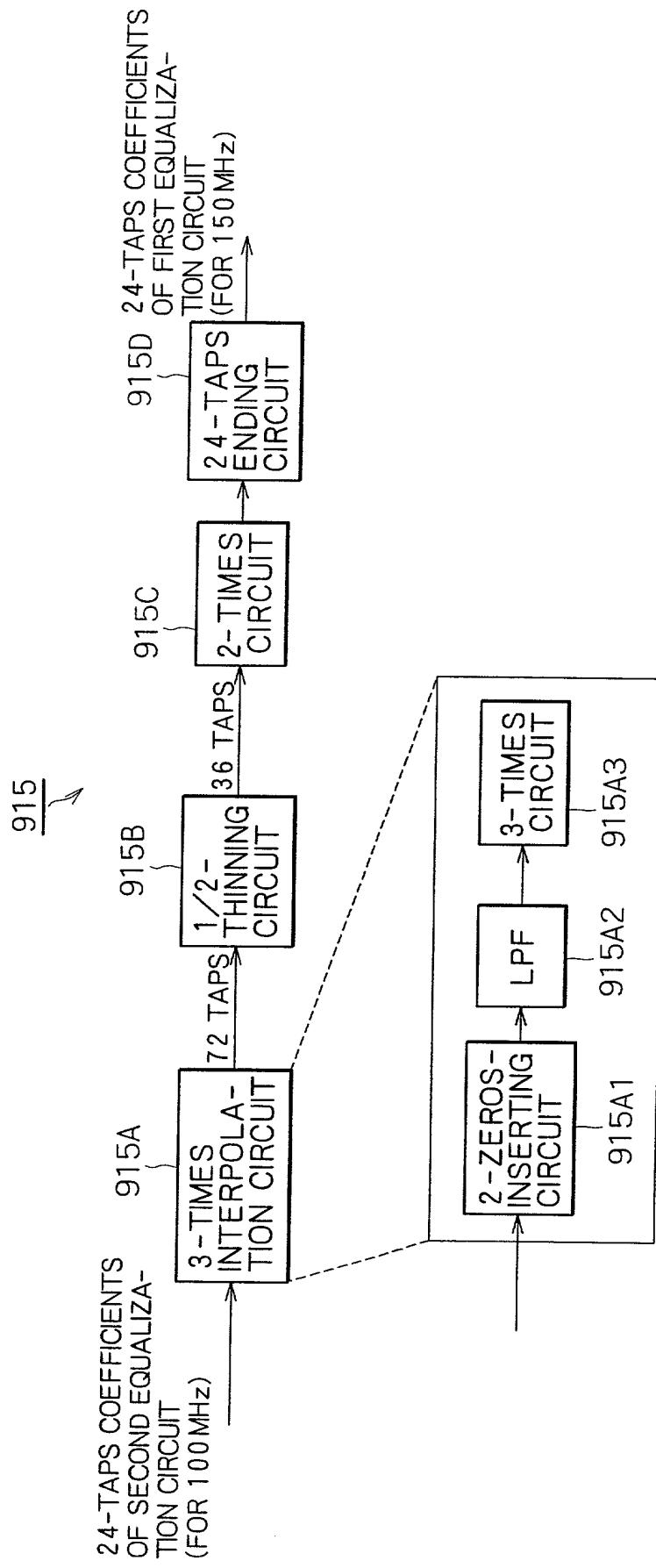


FIG. 58

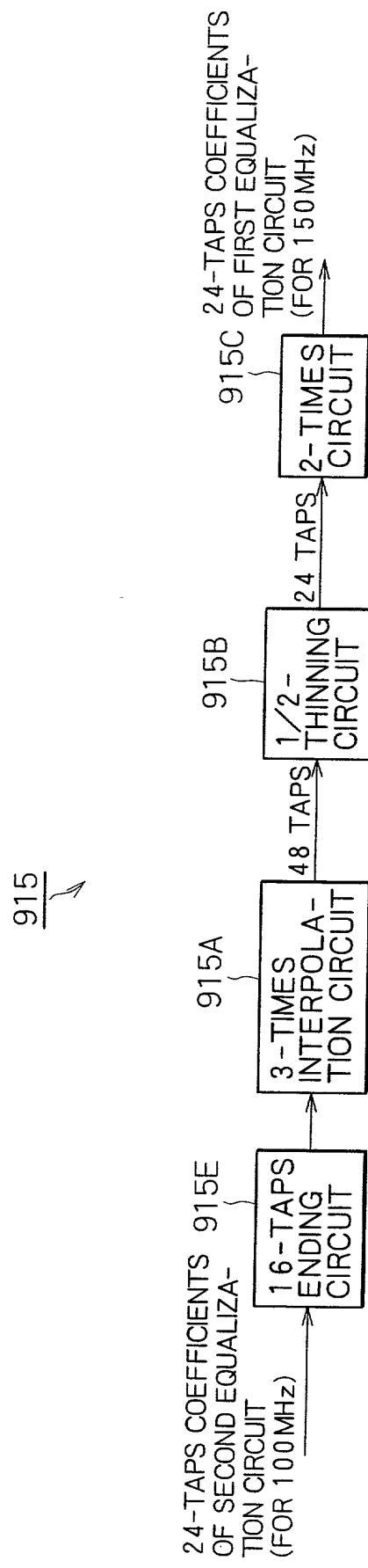


FIG. 59

914

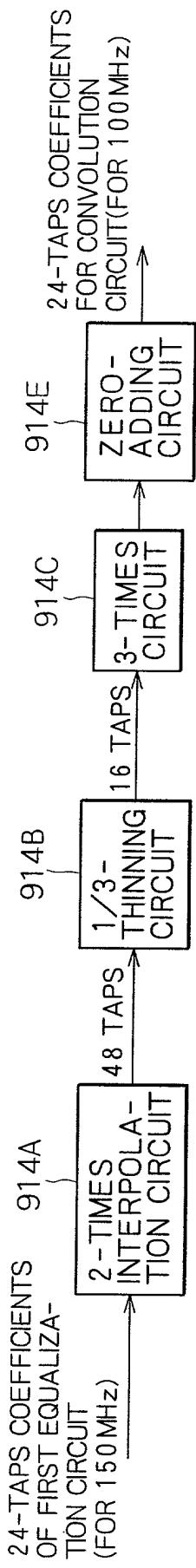


FIG. 60

914

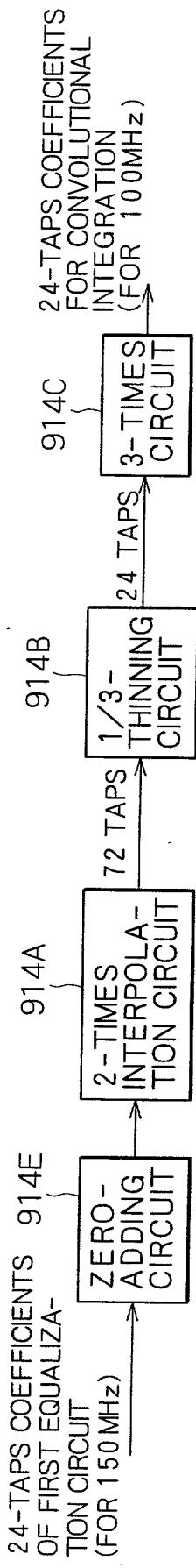


FIG. 61

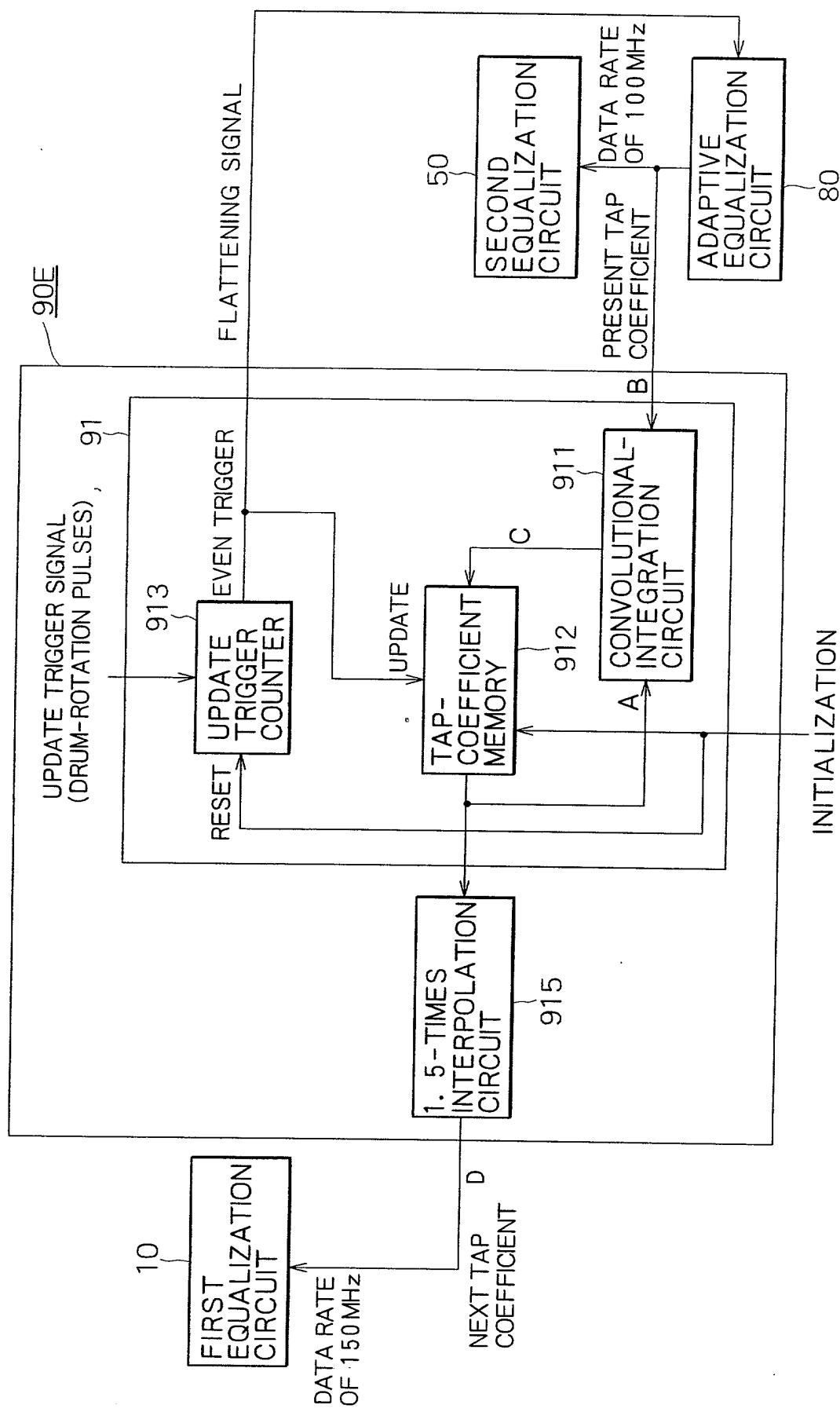


FIG. 62

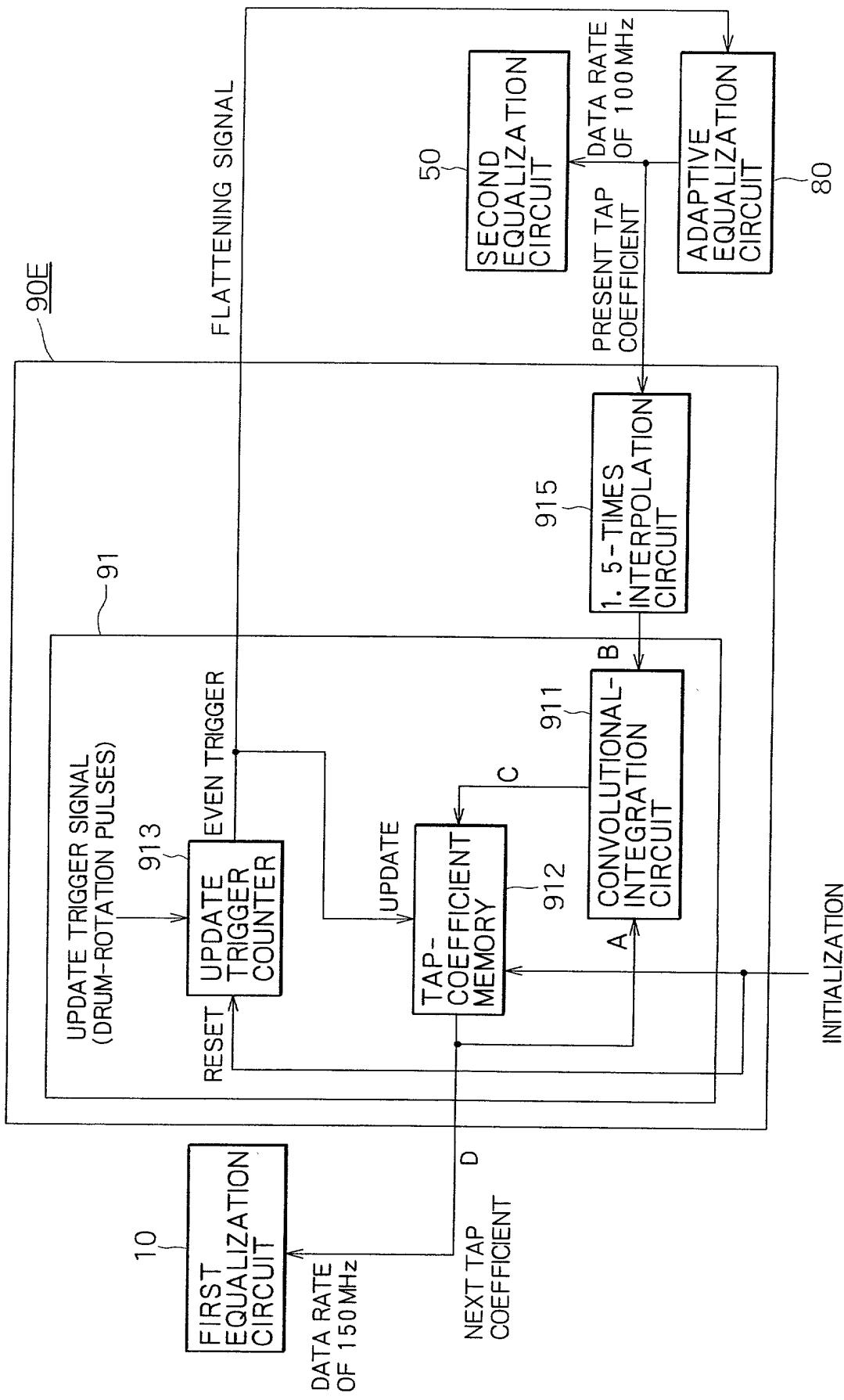


FIG. 63

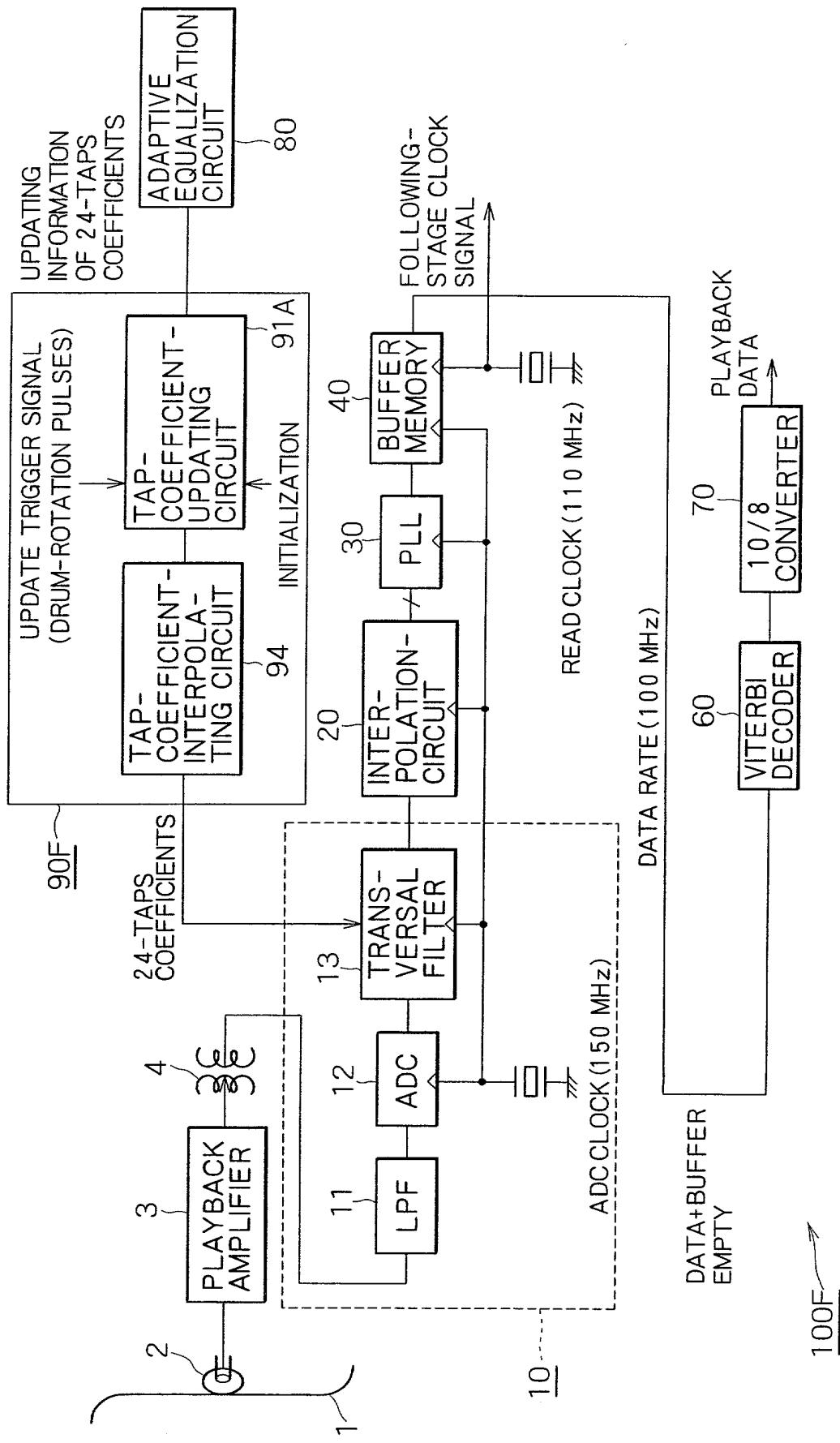


FIG. 64

FIRST EQUALIZATION CIRCUIT

DATA RATE OF 150 MHz

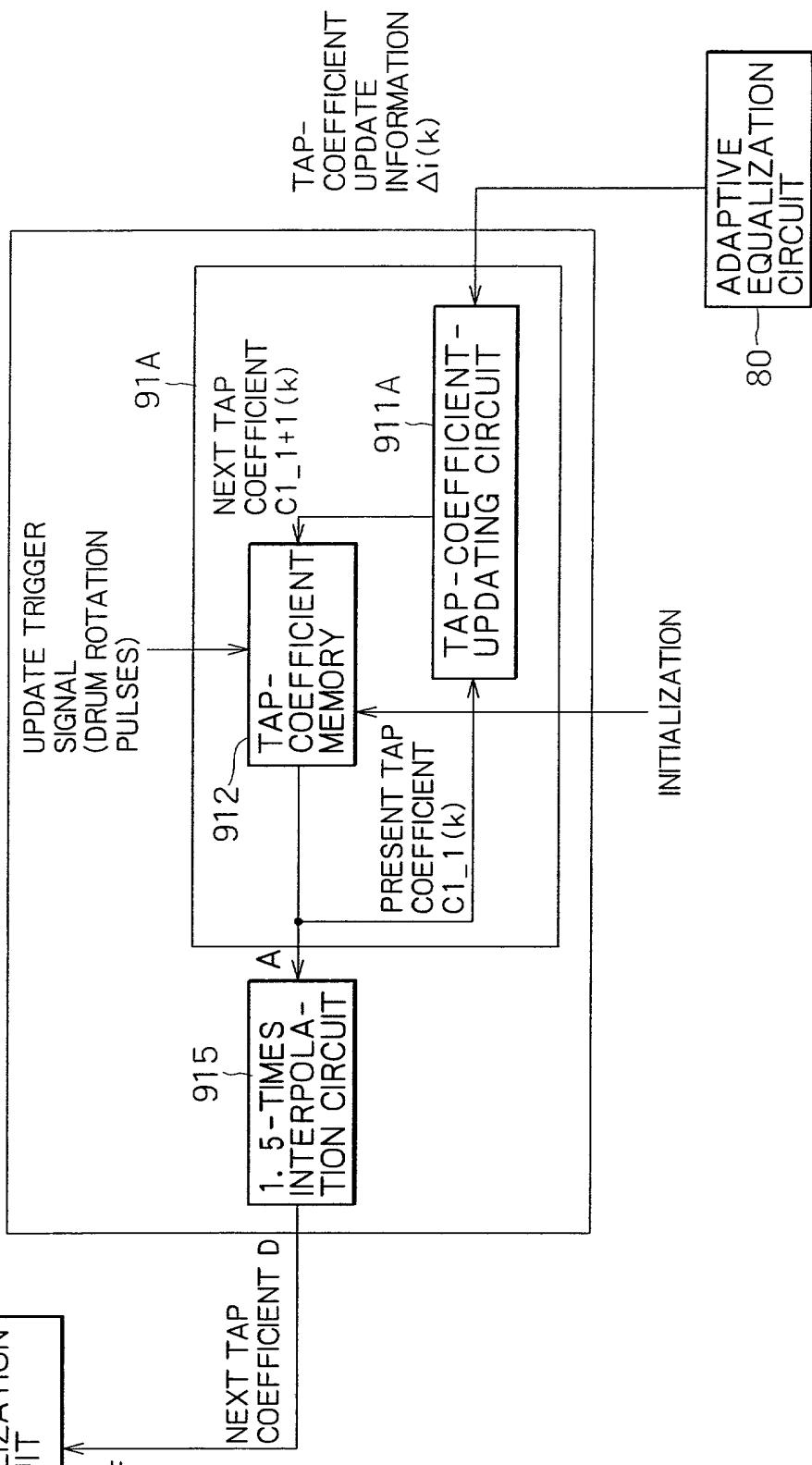


FIG. 65

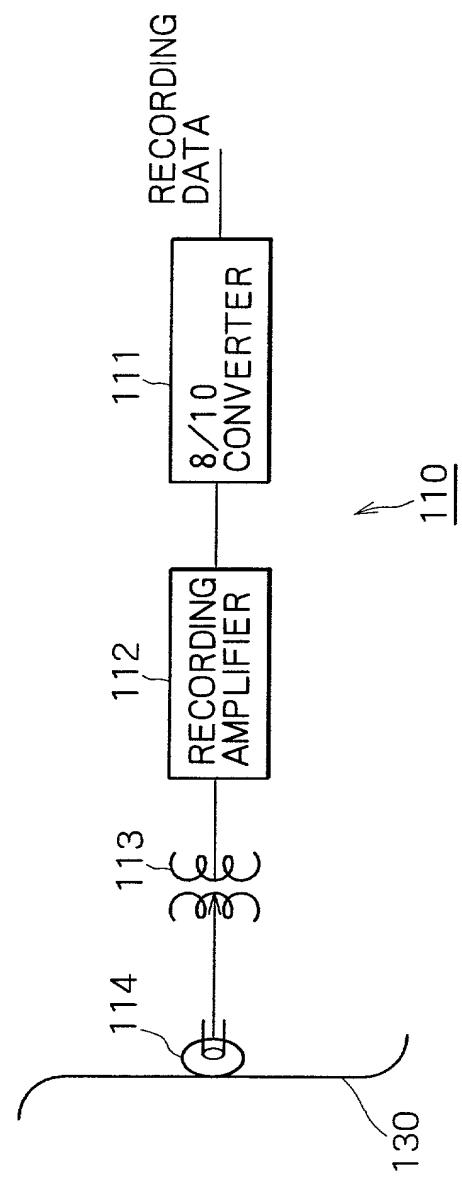


FIG. 66

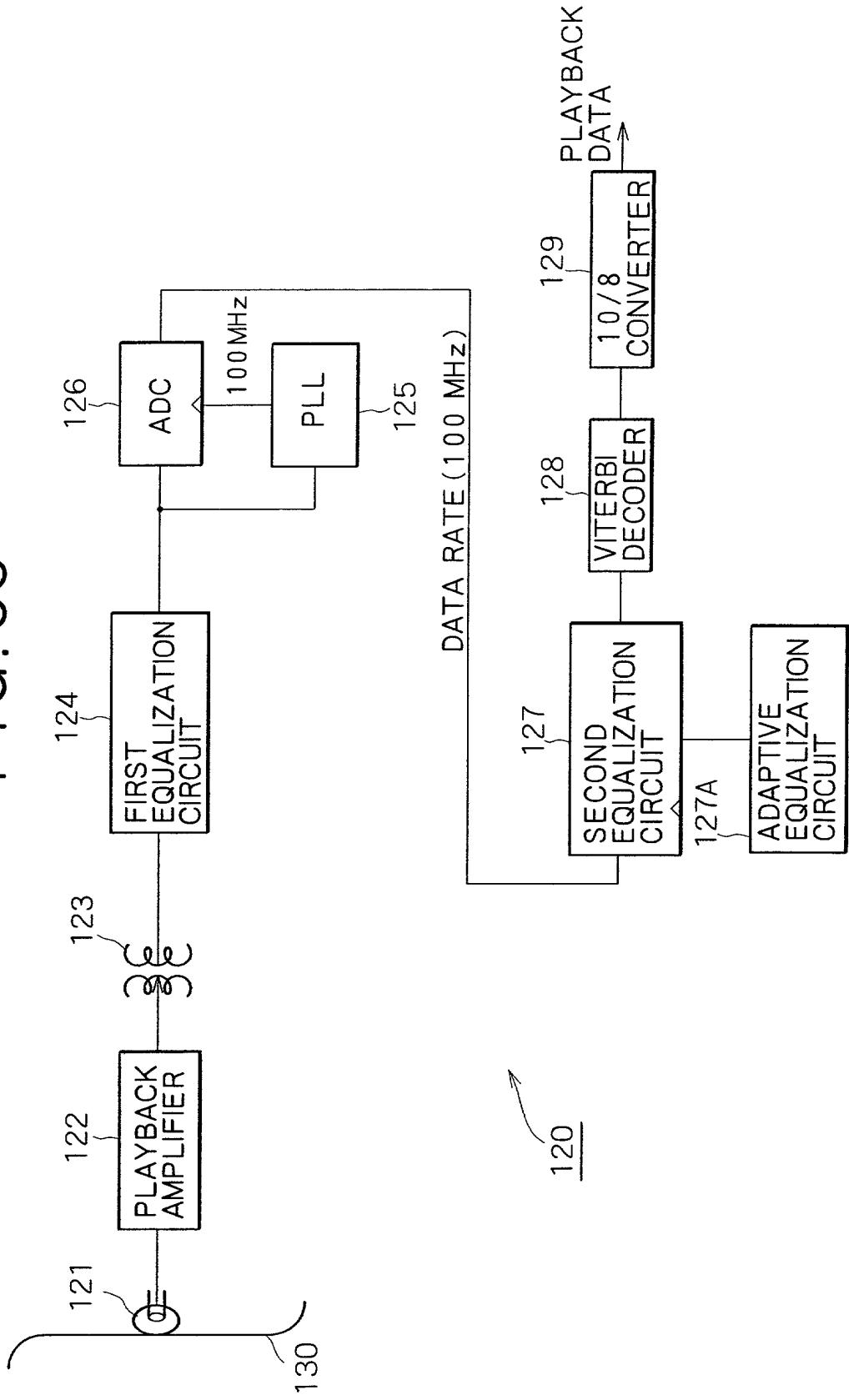
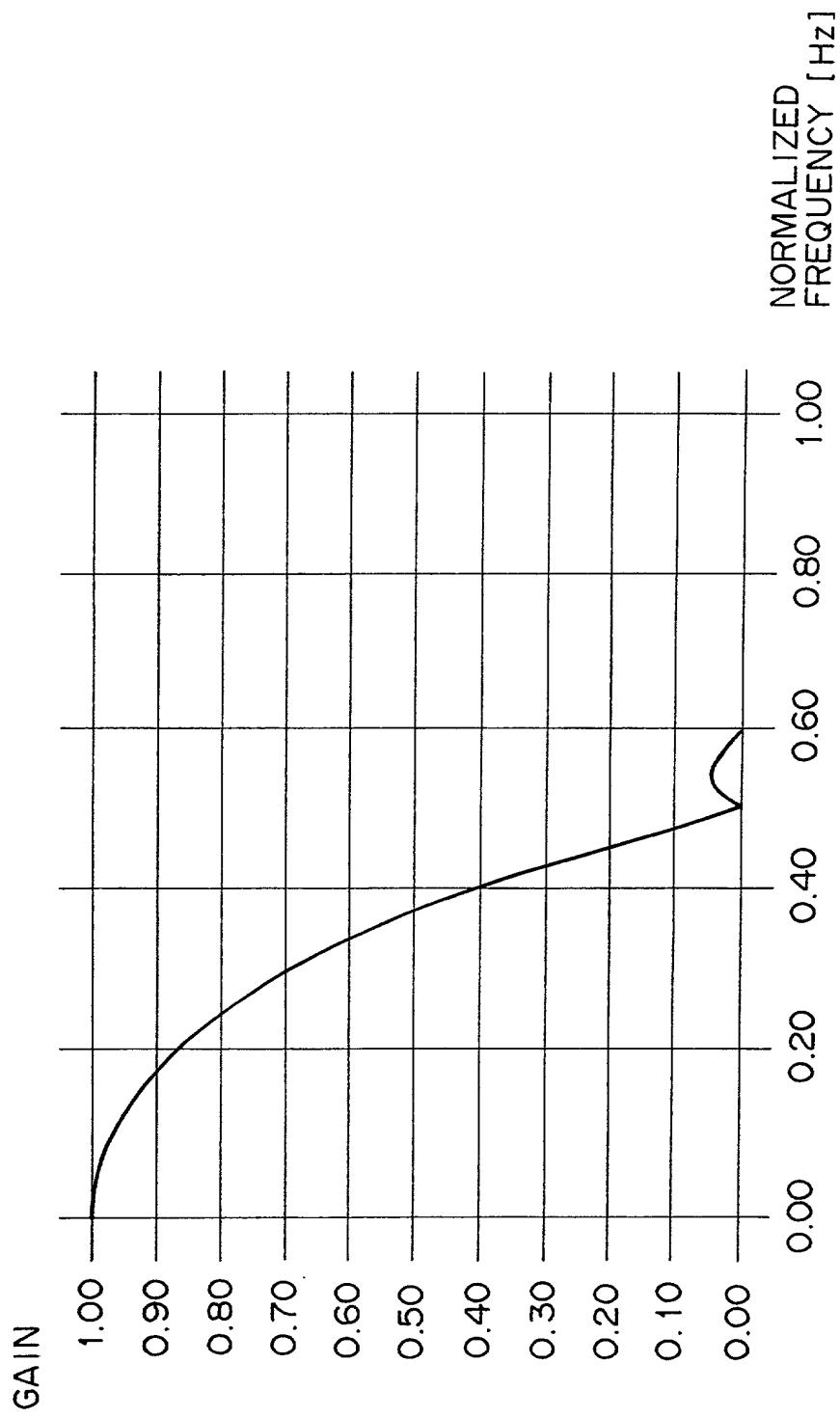


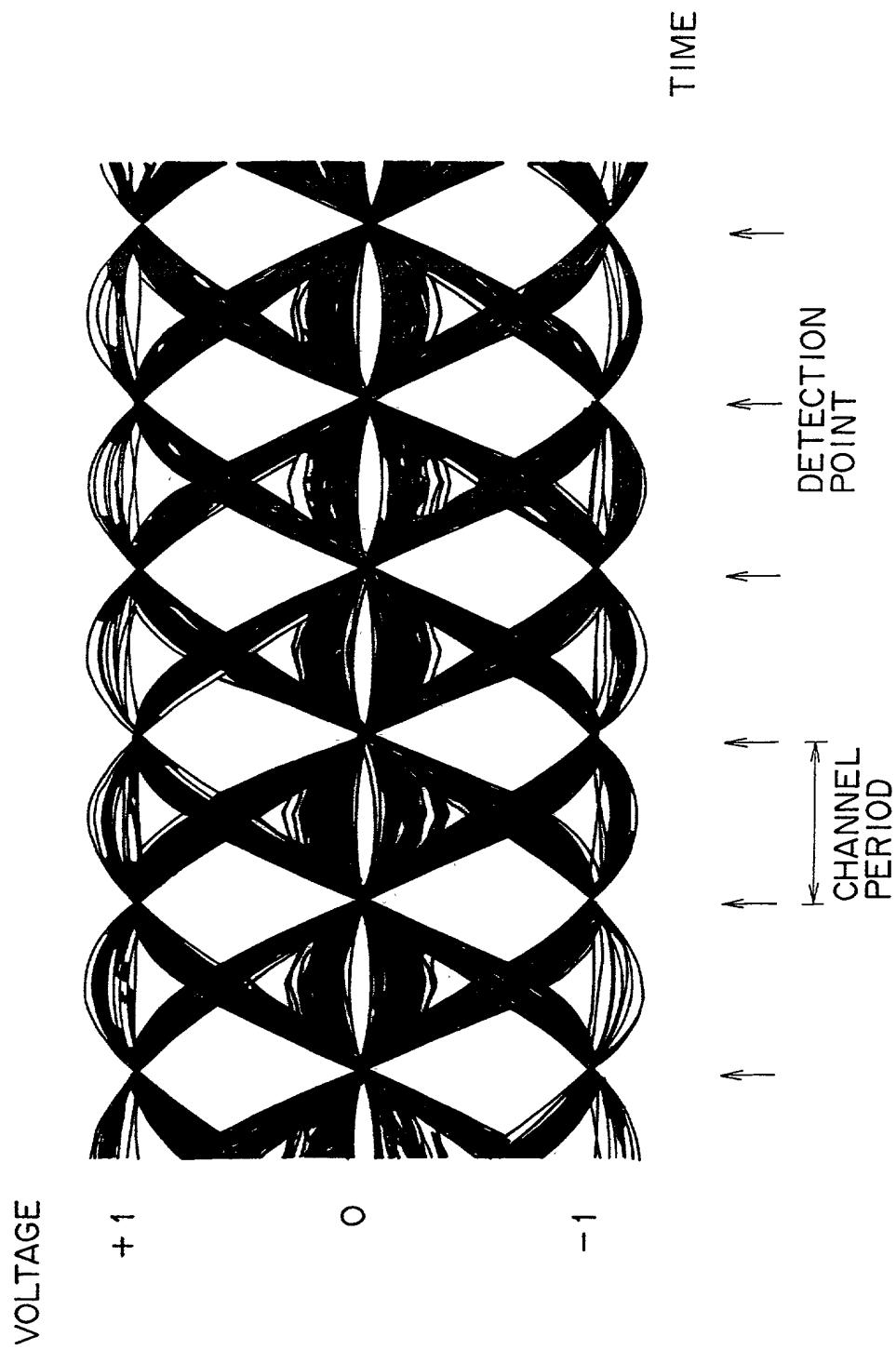
FIG. 67

PR1 CHANNEL CHARACTERISTICS



— 68 .

PR1 CHANNEL EYE PATTERN



Y04777-202667

FIG. 69

PR1 CHANNEL EYE PATTERN

VOLTAGE

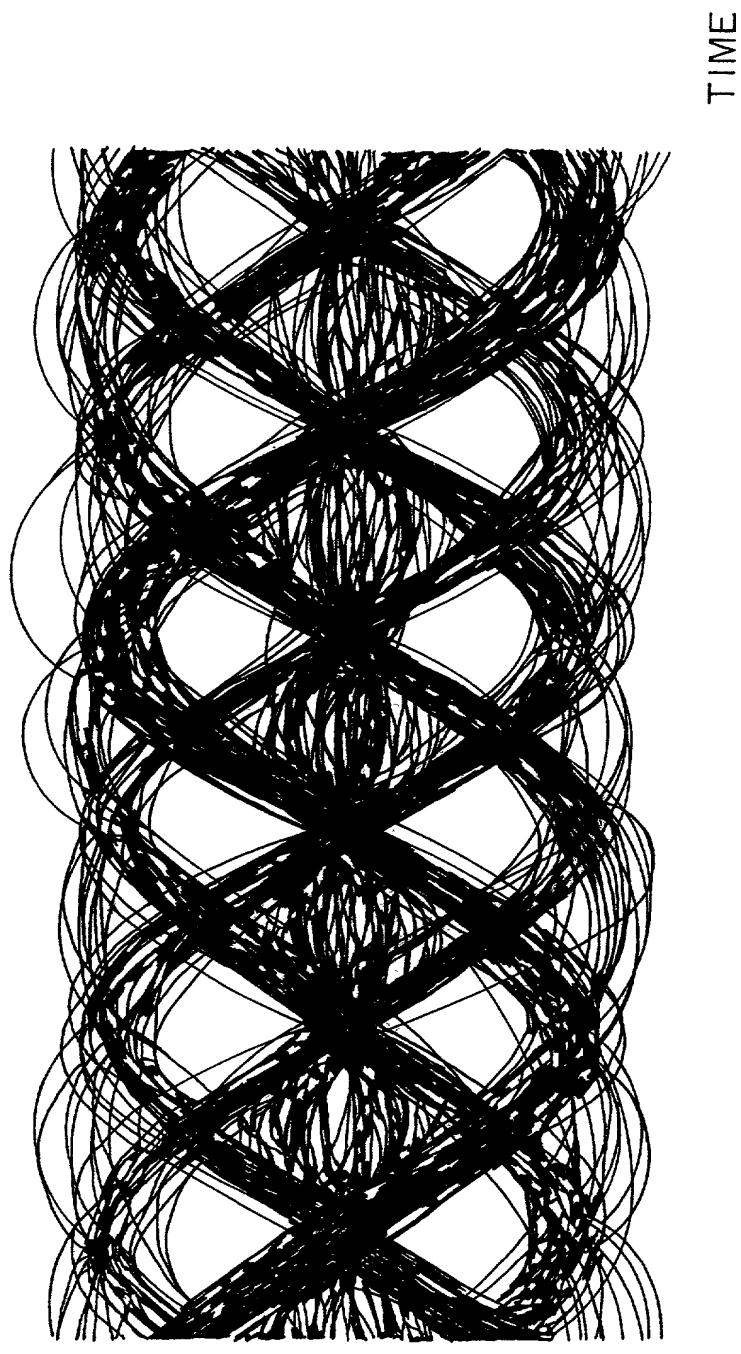


FIG. 70

PR1 CHANNEL EYE PATTERN

VOLTAGE

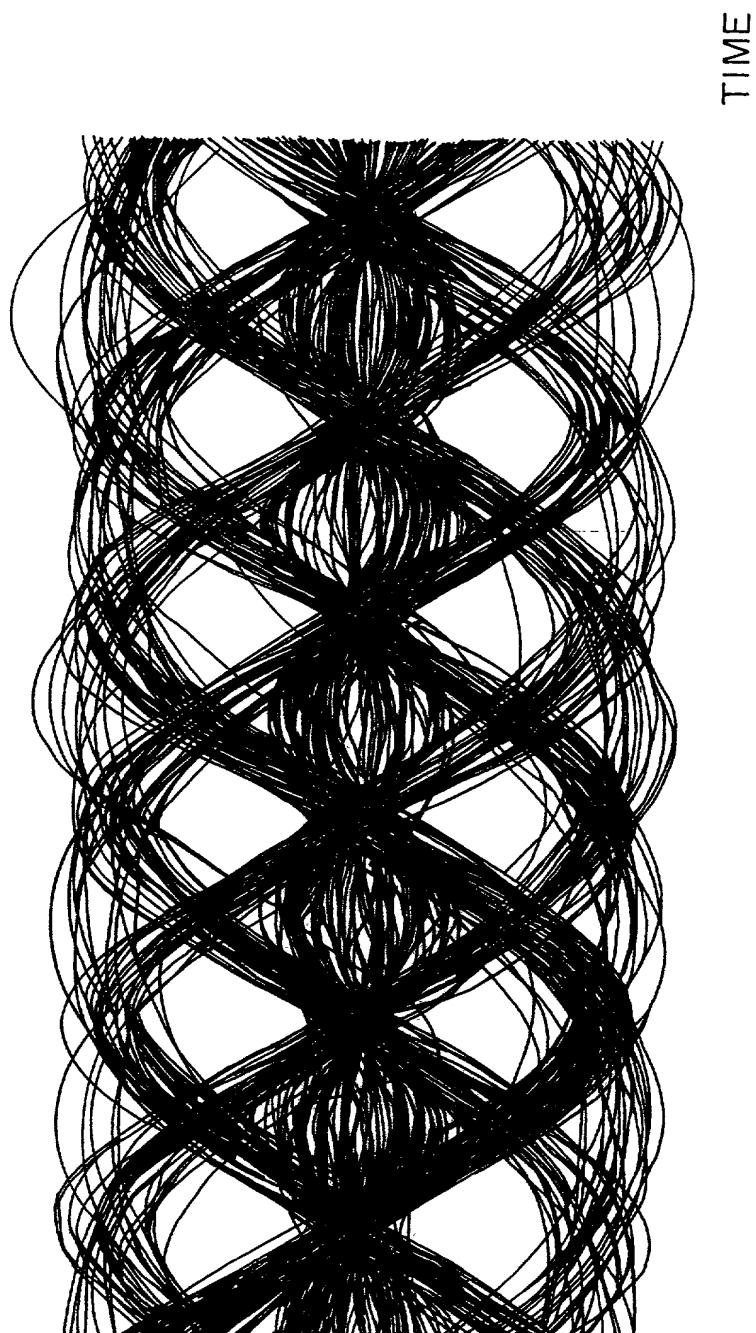


FIG. 71

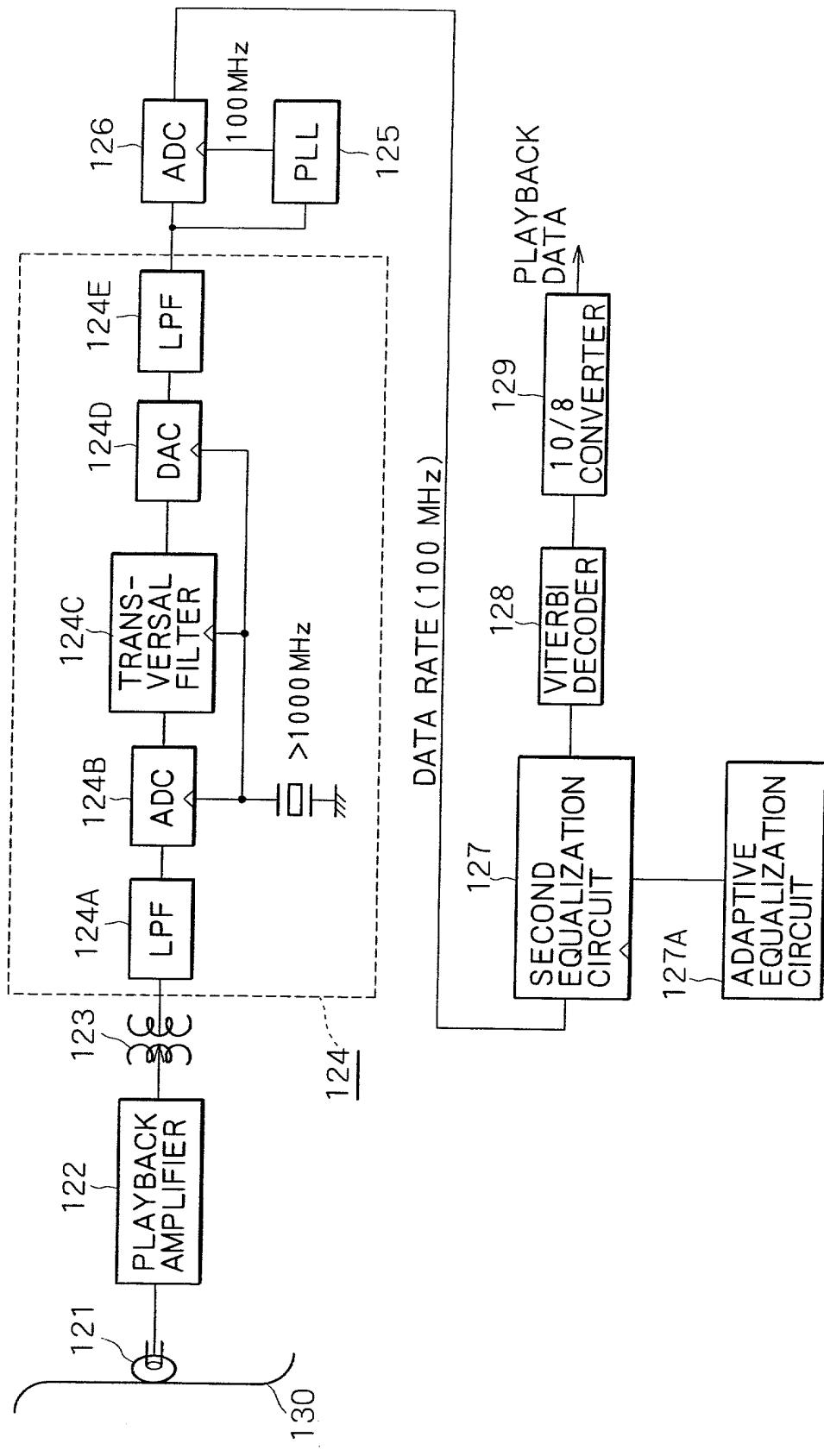


FIG. 72

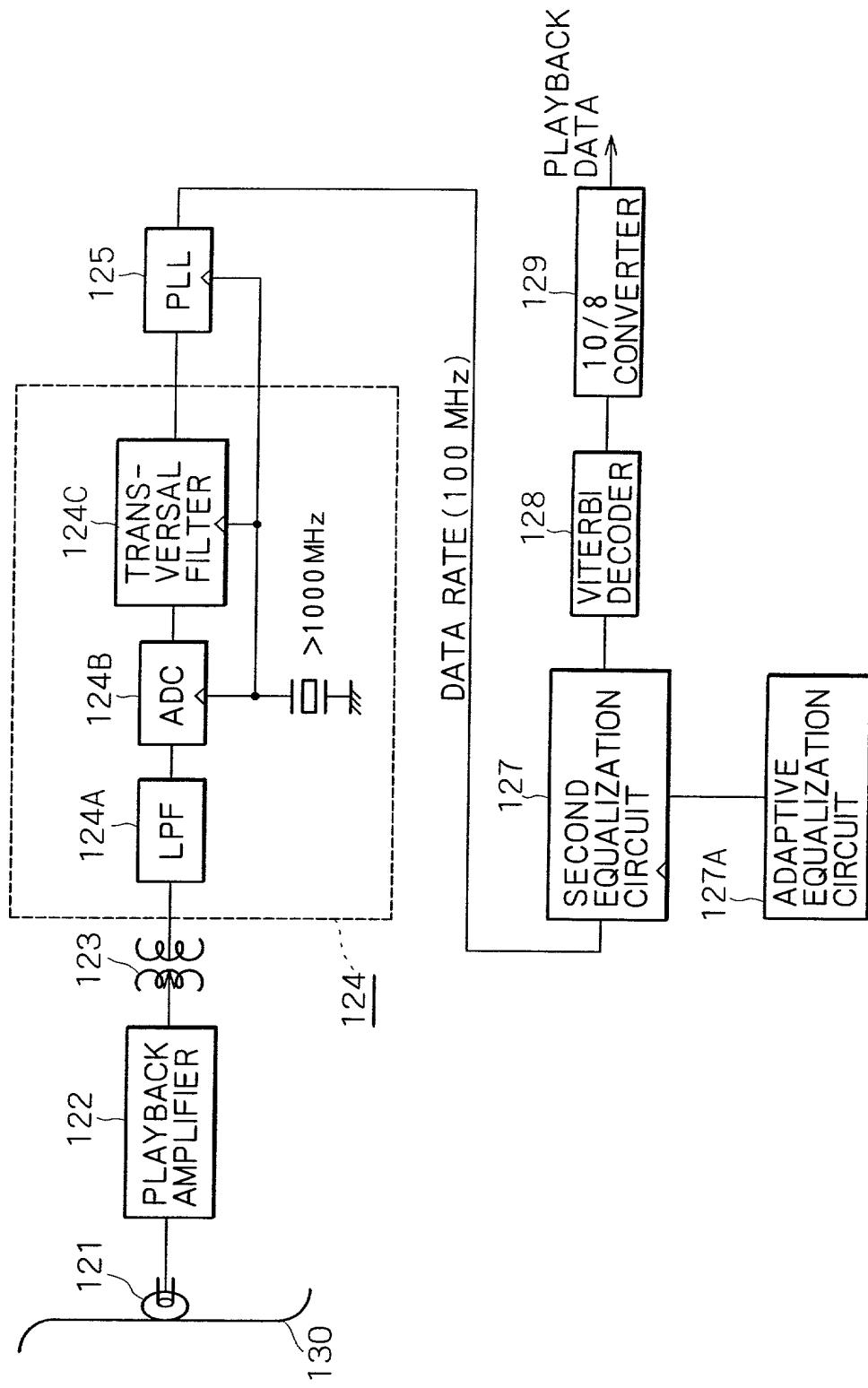


FIG. 73

PR1 EYE PATTERN

VOLTAGE

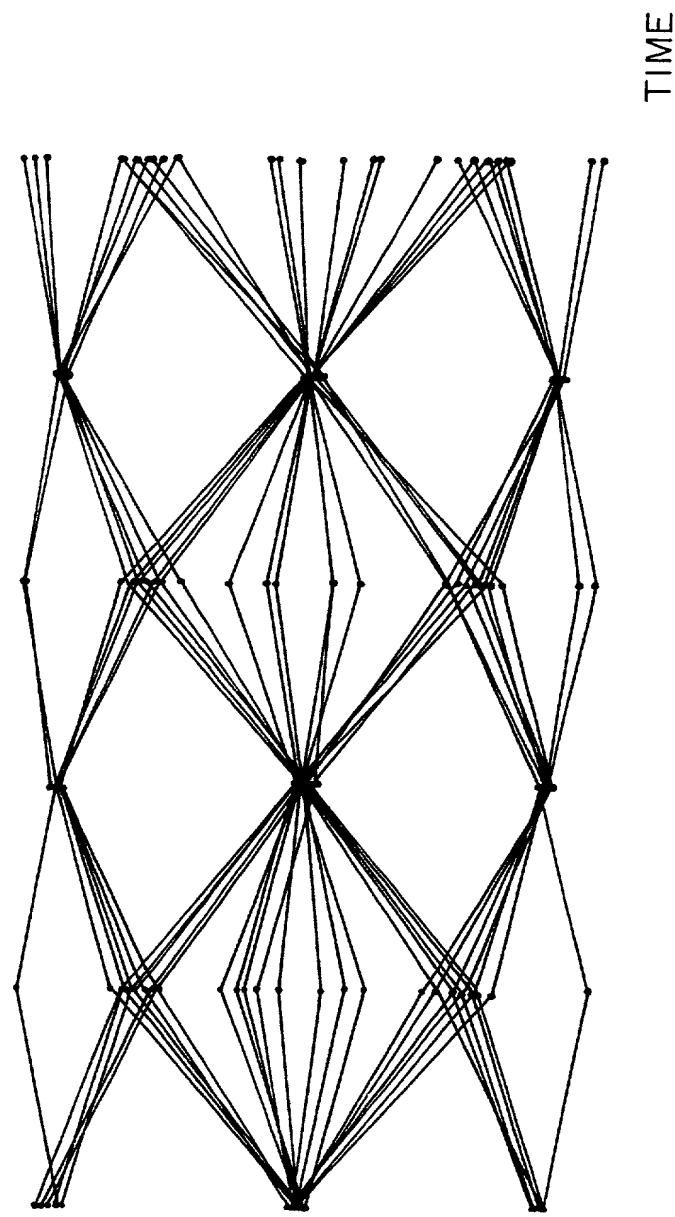


FIG. 74

PR1 EYE PATTERN

VOLTAGE

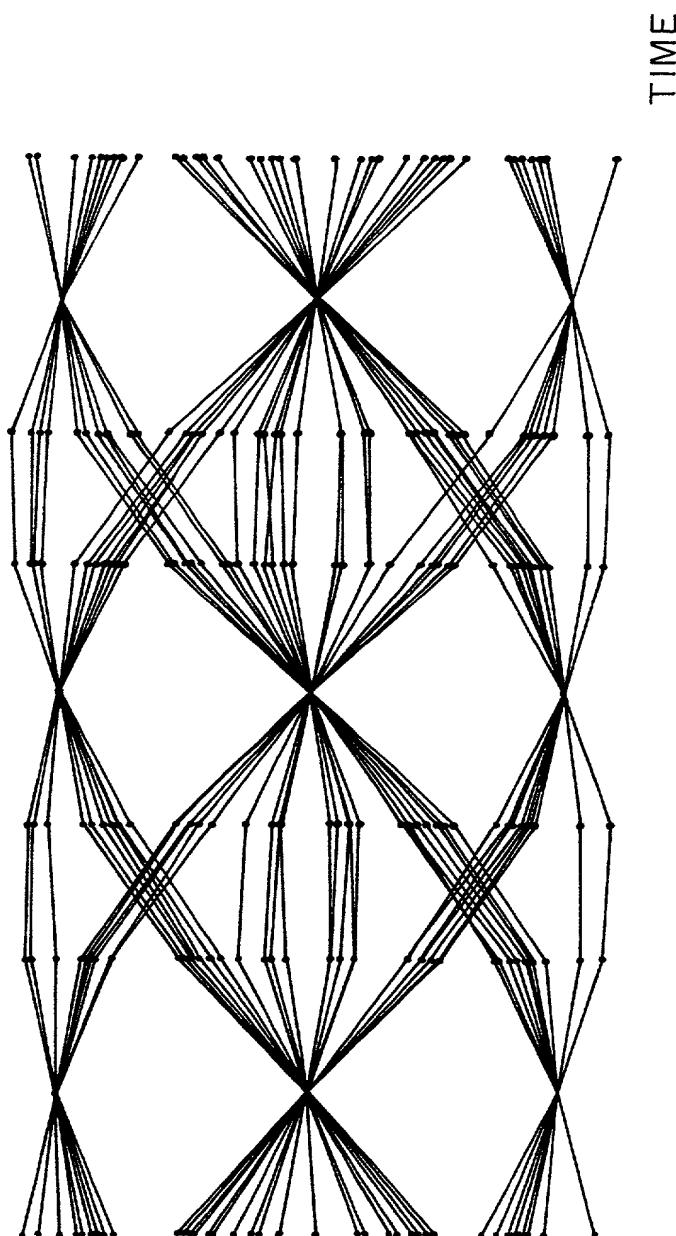


FIG. 75

PR1 EYE PATTERN

VOLTAGE

TIME

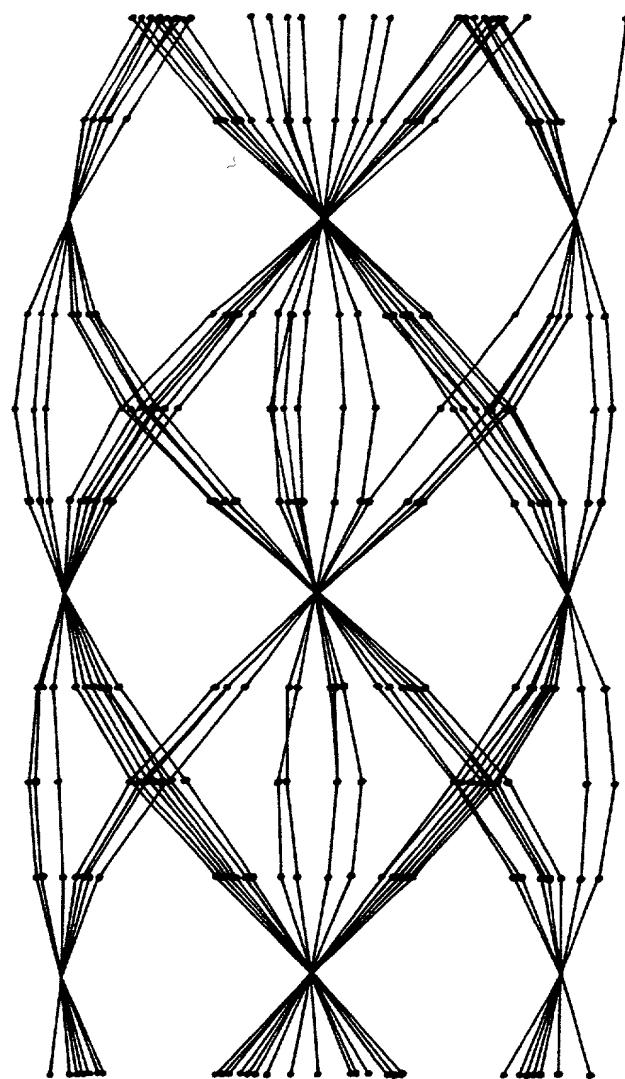


FIG. 76

PR1 EYE PATTERN

VOLTAGE

TIME

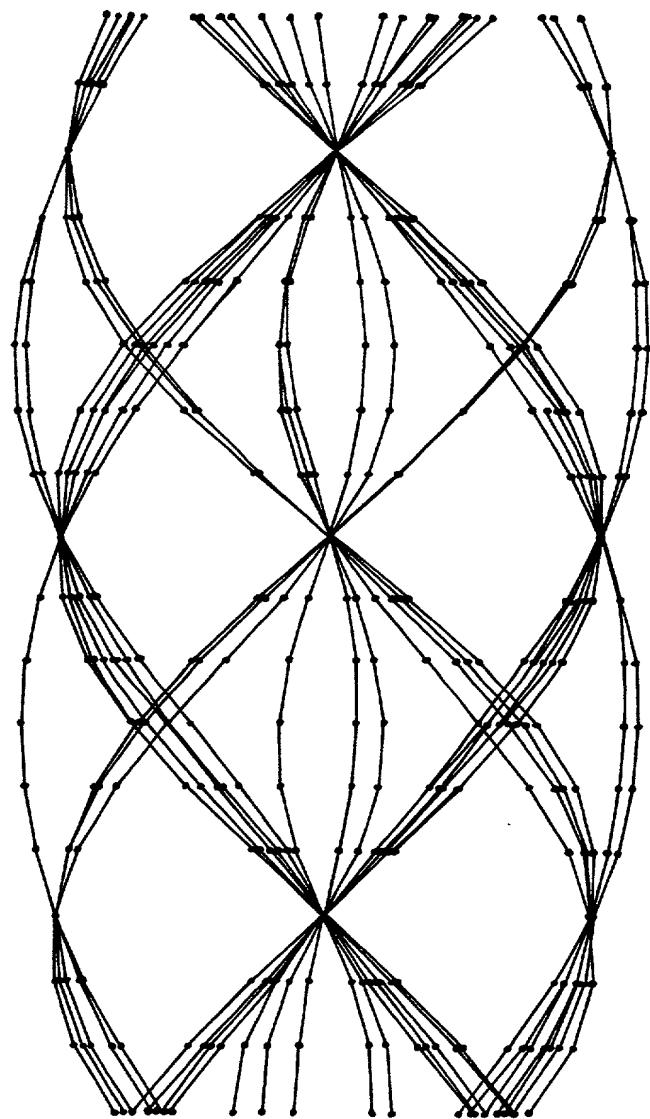


FIG. 77

PR1 EYE PATTERN

